

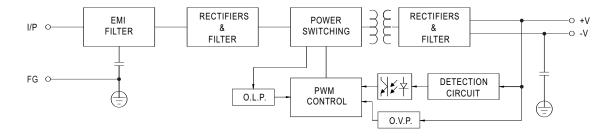
SPECIFICATION

	RSD-30H-3.3	RSD-30H-5	RSD-30H-12	RSD-30H-24	
DC VOLTAGE	3.3V	5V	12V	24V	
RATED CURRENT	6A	6A	2.5A	1.25A	
CURRENT RANGE	0 ~ 6A	0 ~ 6A	0 ~ 2.5A	0 ~ 1.25A	
RATED POWER	19.8W	30W	30W	30W	
RIPPLE & NOISE (max.) Note.2	70mVp-p	70mVp-p	60mVp-p	50mVp-p	
VOLTAGE TOLERANCE Note.3	±2.0%		±2.0%	±2.0%	
LINE REGULATION	±0.5%	±0.5%	±0.3%	±0.2%	
LOAD REGULATION	±0.5%	±0.5%	±0.3%	±0.2%	
SETUP, RISE TIME	120ms, 85ms at full load				
VOLTAGE RANGE CONTINUOUS	7				
, , , ,			1 00 70	0070	
		0.00741104			
OVERLOAD OVER VOLTAGE					
				27.6 ~ 32.4V	
				21.0 ~ 32.4 V	
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	, ,	00	7 M	t- IF004070	
	10 ~ 500Hz, 5G 10min./1cycle, 60min. each along X, Y, Z axes; Mounting: compliance to IEC61373				
ISOLATION RESISTANCE					
EMC EMISSION 7 & EMC IMMUNITY				Class A	
				Class B	
				Class A	
	-				
			2	Test Level / Note	
				Level 3, ±8KV air; Level 3, ±6KV contact	
	Radiated Field	EN01000-4-	ა	Level 3, 2KV at power	
	EFT / Burst	T / Burst EN61000-4-4		<u> </u>	
	0	51104000 4 5		Level 4, 2KV at signal	
				Level 3,1KV Line-Line, Level 3, 2KV Line-Earth	
DAIL WAY OTANDA DD				Level 3	
	Compliance to EN45545-2 for fire protection; EN50155 / IEC60571 including IEC61373 for shock & vibration, EN50121-3-2 for EMC				
PACKING	U.25Kg; 5bpcs/15Kg/U.83CUF1				
Ripple & noise are measure Tolerance : includes set up The power supply is consid a 360mm*360mm metal pla perform these EMC tests, p	ed at 20MHz of bandwidth by tolerance, line regulation and ered a component which will be the with 1mm of thickness. The blease refer to "EMI testing of o	using a 12" twisted pair-valuation. load regulation. be installed into a final economic of the second of the sec	vire terminated with a 0.1ur uipment. All the EMC tests e re-confirmed that it still m es." (as available on http://w	f & 47uf parallel capacitor. s are been executed by mounting the unit on leets EMC directives. For guidance on how to	
	RATED CURRENT CURRENT RANGE RATED POWER RIPPLE & NOISE (max.) Note.2 VOLTAGE TOLERANCE Note.3 LINE REGULATION LOAD REGULATION SETUP, RISE TIME HOLD UP TIME (Typ.) VOLTAGE RANGE CONTINUOUS EFFICIENCY (Typ.) DC CURRENT (Typ.) INRUSH CURRENT (Typ.) OVERLOAD OVER VOLTAGE WORKING TEMP. WORKING HUMIDITY STORAGE TEMP., HUMIDITY TEMP. COEFFICIENT VIBRATION SAFETY STANDARDS WITHSTAND VOLTAGE ISOLATION RESISTANCE EMC EMISSION EMC IMMUNITY RAILWAY STANDARD MTBF DIMENSION PACKING 1. All parameters NOT special 2. Ripple & noise are measure 3. Tolerance: includes set up 4. The power supply is consid a 360mm*360mm metal pla perform these EMC tests, p	DC VOLTAGE	DC VOLTAGE	DC VOLTAGE 3.3V 5V 12V RATED CURRENT 6A 6A 2.5A CURRENT RANGE 0 - 6A 0 - 6A 0 - 6A 0 - 2.5A CURRENT RANGE 19.8W 30W 30W 30W RIPPLE & NOISE (max.) Note.2 70mVp-p 70mVp-p 60mVp-p 60mVp-p VOLTAGE TOLERANCE Note.3 ± 2.0% ± 2.0% ± 2.0% ± 2.0% ± 2.0% ± 2.0% ± 0.3% ± 0.5% ± 0.5% ± 0.3% ± 0.5% ± 0.3% ± 0.5% ± 0.5% ± 0.3% ± 0.5% ± 0.5% ± 0.3% ± 0.5% ± 0.5% ± 0.5% ± 0.3% ± 0.5	



■ Block Diagram

fosc: 110KHz



■ Input Fuse

There is one fuse connected in series to the positive input line, which is used to protect against abnormal surge. Fuse specifications of each model are shown as below.

Type	Fuse Type	Reference and Rating
G	Time-Lag	CONQUE MST, 6.3A, 250V
L	Time-Lag	CONQUE MST, 3.15A, 250V
Н	Time-Lag	CONQUE MST, 2A, 250V

■ Input Reverse Polarity Protection

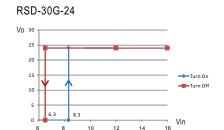
There is a MOSFET connected in series to the negative input line. If the input polarity is connected reversely, the MOSFET opens and there will be no output to protect the unit.

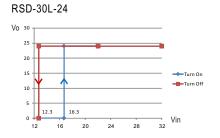
■ Input Range and Transient Ability

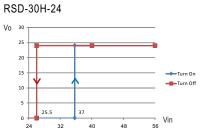
The series has a wide range input capability. With $\pm 40\%$ of rated input voltage, it can withstand that for 1 second.

■ Input Under-Voltage Protection

If input voltage drops below Vimin, the internal control IC shuts down and there is no output voltage. It recovers automatically when input voltage reaches above Vimin, please refer to the cruve below.







■ Inrush Current

Inrush current is suppressed by a resistor during the initial start-up, and then the resistor is bypassed by a MOSFET to reduce power consumption after accomplishing the start-up.