

# Low Power, <100 Lux Optimized, Analog Output Ambient Light Sensor

## ISL76671

The [ISL76671](#) is a low cost, light-to-voltage silicon optical sensor combining a photodiode array, a non-linear current amplifier and a micro-power op amp on a single monolithic IC. Similar to the human eye, the photodiode array has peak sensitivity at 550nm and spans the wavelength range 400nm to 600nm, rejecting UV and IR light. The input luminance range is from 0.01 lux to 100 lux.

The integrated non-linear current amplifier boosts and converts the photodiode signal into a square root output format, extending dynamic range while maintaining excellent sensitivity in dimly lit conditions. As such, the part is ideal for measuring incident daylight when mounted behind heavily smoked bezels used around displays or behind mirrors.

The device consumes minimum power. A dark current compensation circuit minimizes the effect of temperature dependent leakage currents in the absence of light, improving the light sensitivity at low lux levels. The output gain has been optimized to require a relatively low value external bias resistor that falls within recommended automotive EMI limits. The built-in 1µA op amp gives the ISL76671 an output voltage driving advantage for heavier loads that can drive an ADC directly.

The ISL76671 is housed in an ultra compact 2mmx2.1mm ODFN plastic surface mount package. Operation is rated from -40°C to +105°C, Grade 2 per AEC-Q100.

## Features

- Square root voltage output
- 0.01 lux to 100 lux range
- 1.8V to 3V supply range
- Close to human eye spectral response
- Fast response time
- Internal temperature compensation
- Good IR rejection
- Low supply current
- Operating temperature range -40°C to +105°C
- 6 Ld ODFN: 2mmx2.1mmx0.7mm
- Pb-free (RoHS compliant)
- AEC-Q100 Qualified

## Applications

- Display backlight control - central info display and instrumentation
- Anti-glare mirror systems - specified to operate behind Bezel mounting

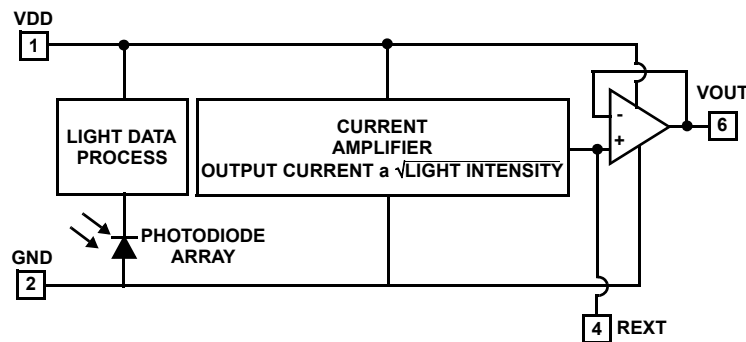
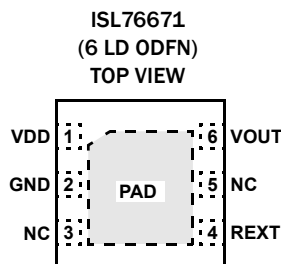


FIGURE 1. SIMPLIFIED BLOCK DIAGRAM

# ISL76671

## Pin Configuration



## Pin Descriptions

PIN NUMBER	PIN NAME	PIN DESCRIPTION
1	VDD	Voltage Supply (1.8V to 3V).
2	GND	Ground
3, 5	NC	No connect
4	REXT	Connected to an external resistor to GND, setting the light-to-voltage scaling constant. A $R_{EXT}$ value of 100k $\Omega$ is recommended.
6	VOUT	Voltage Output.
-	PAD	Thermal Pad. The thermal pad can be connected to GND or electrically isolated.

## Ordering Information

PART NUMBER (Notes 1, 2, 3)	PACKAGE Tape and Reel (RoHS Compliant)	PKG. DWG. #
ISL76671AR0Z-T7	6 Ld ODFN (Tape and Reel)	L6.2X2.1
ISL76671AR0Z-T7A	6 Ld ODFN (Tape and Reel)	L6.2X2.1
ISL76671EVAL1Z	Evaluation Board	

### NOTES:

1. Please refer to [TB347](#) for details on reel specifications.
2. These Intersil Pb-free plastic packaged products employ special Pb-free material sets; molding compounds/die attach materials and NiPdAu plate -e4 termination finish, which is RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.
3. For Moisture Sensitivity Level (MSL), please see device information page for [ISL76671](#). For more information on MSL please see techbrief [TB477](#).