TOSHIBA GTR Module Silicon N Channel IGBT

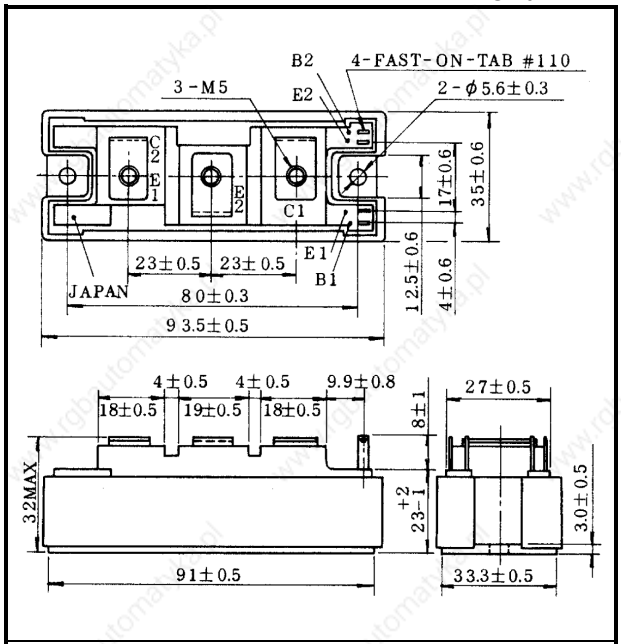
MG50Q2YS40

High Power Switching Applications.

Motor Control Applications.

- High input impedance
- High speed: tf = 0.5μs (max.)
  trr = 0.5μs (max.)
- Low saturation voltage
  : $V_{CE(sat)}$ = 4.0V (max.)
- Enhancement−mode
- Includes a complete half bridge in one package.
- The electrodes are isolated from case.

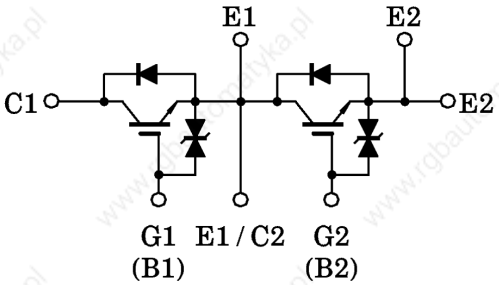
Unit in mm



JEDEC	—
JEITA	—
TOSHIBA	2−94D1A

Weight: 202g

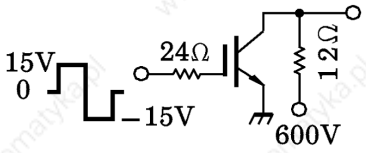
Equivalent Circuit



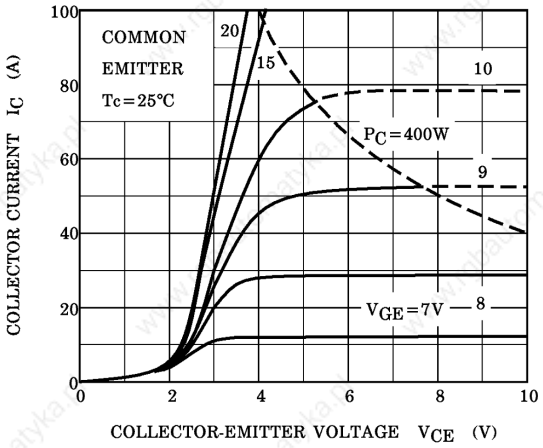
Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Collector−emitter voltage		V_{CES}	1200	V
Gate−emitter voltage		V_{GES}	±20	V
Collector current	DC	I_C	50	A
	1ms	I_{CP}	100	
Forward current	DC	I_F	50	A
	1ms	I_{FM}	100	
Collector power dissipation (Tc = 25°C)		P_C	400	W
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−40~125	°C
Isolation voltage		V_{Isol}	2500 (AC 1 minute)	V
Screw torque (terminal / mounting)		—	3 / 3	N·m

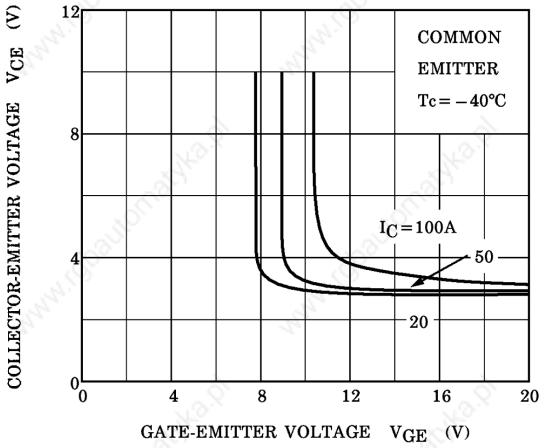
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
Gate leakage current		I_{GES}	V_{GE} = ±20V, V_{CE} = 0	—	—	±10	μA
Collector cut−off current		I_{CES}	V_{CE} = 1200V, V_{GE} = 0	—	—	1.0	mA
Gate−emitter cut−off voltage		$V_{GE(off)}$	I_C = 50mA, V_{CE} = 5V	3.0	—	6.0	V
Collector−emitter saturation voltage		$V_{CE(sat)}$	I_C = 50A, V_{GE} = 15V	—	3.0	4.0	V
Input capacitance		C_{ies}	V_{CE} = 10V, V_{GE} = 0, f = 1MHz	—	6000	—	pF
Switching time	Rise time	t_r		—	0.3	0.6	μs
	Turn−on time	t_{on}		—	0.4	0.8	
	Fall time	t_f		—	0.2	0.5	
	Turn−off time	t_{off}		—	0.8	1.5	
Forward voltage		V_F	I_F = 50A, V_{GE} = 0	—	2.0	2.5	V
Reverse recovery time		t_{rr}	I_F = 50A, V_{GE} = −10V, di / dt = 100A / μs	—	0.25	0.5	μs
Thermal resistance		$R_{th(j-c)}$	Transistor	—	—	0.31	°C / W
			Diode	—	—	1.0	

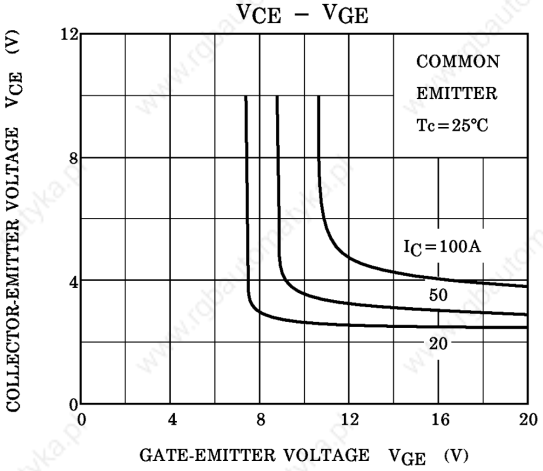
I_C – V_{CE}



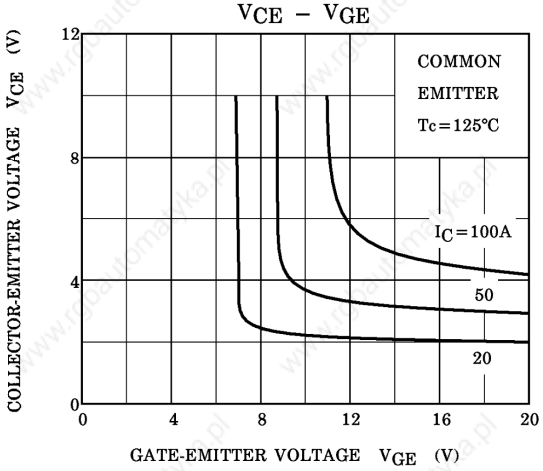
V_{CE} – V_{GE}



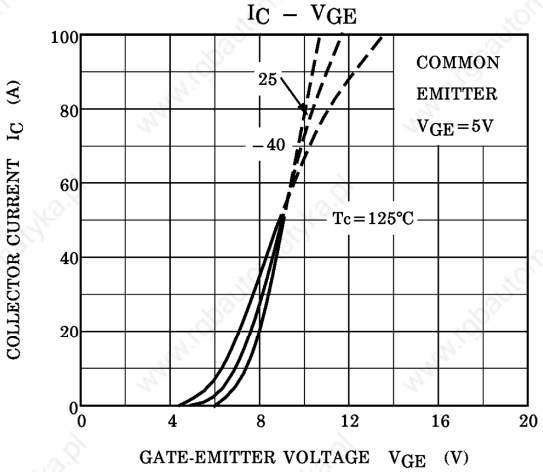
V_{CE} – V_{GE}



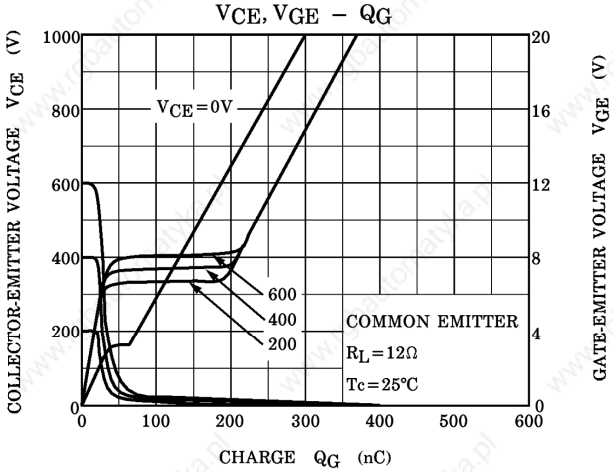
V_{CE} – V_{GE}



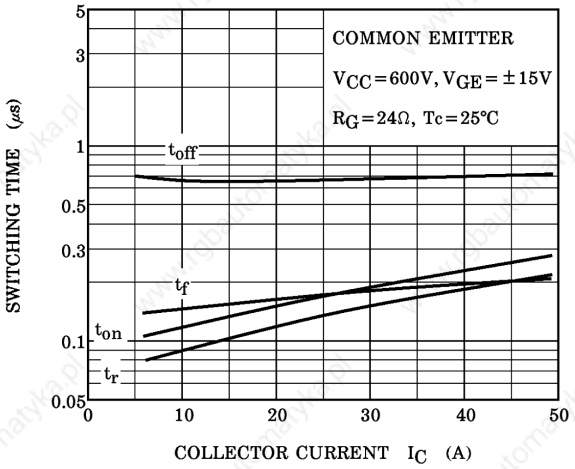
I_C – V_{GE}



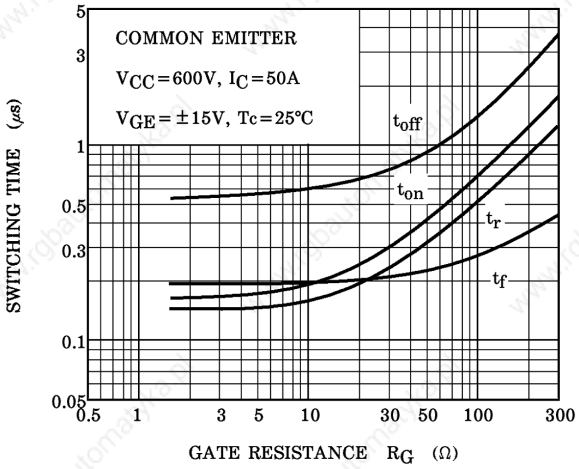
V_{CE}, V_{GE} – Q_G



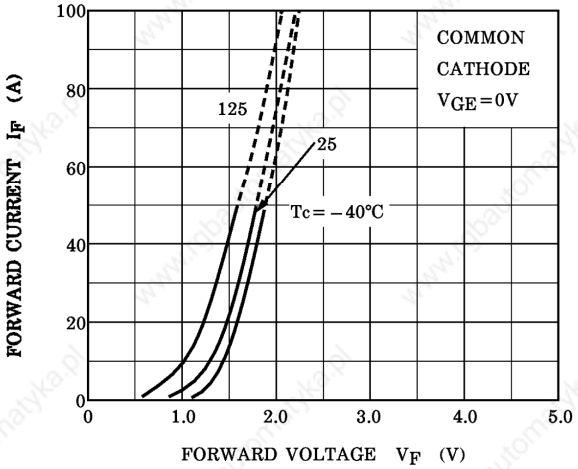
SWITCHING TIME – I_C



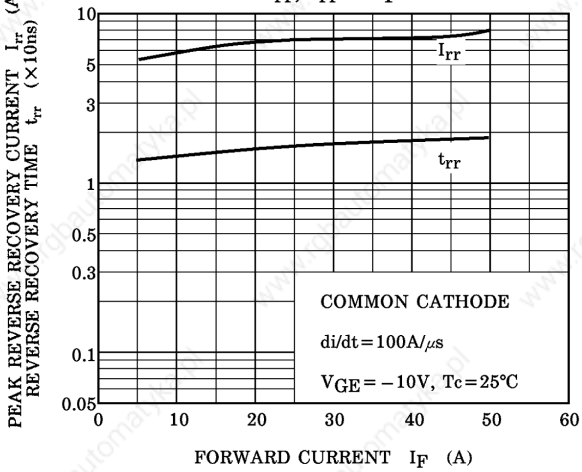
SWITCHING TIME – R_G



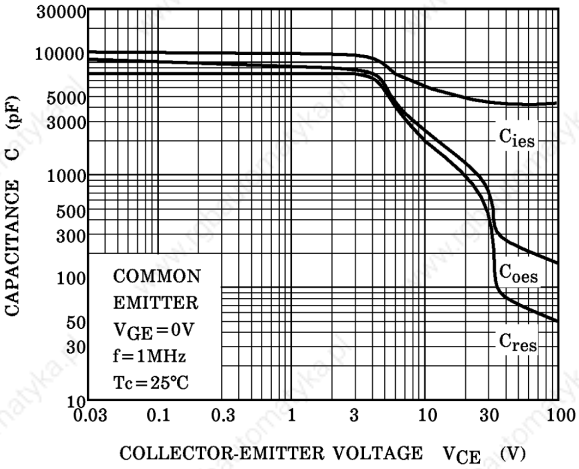
I_F – V_F



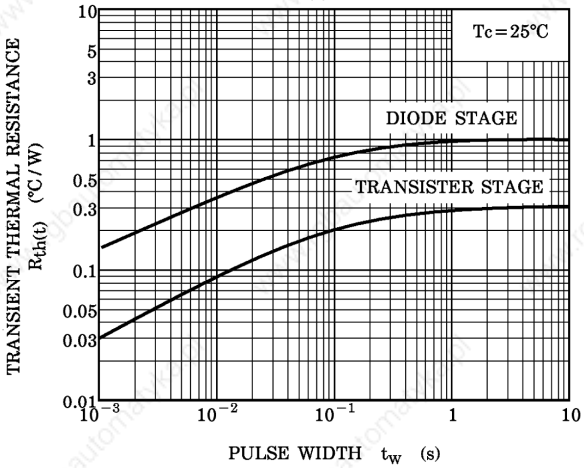
t_{rr}, I_{rr} – I_F



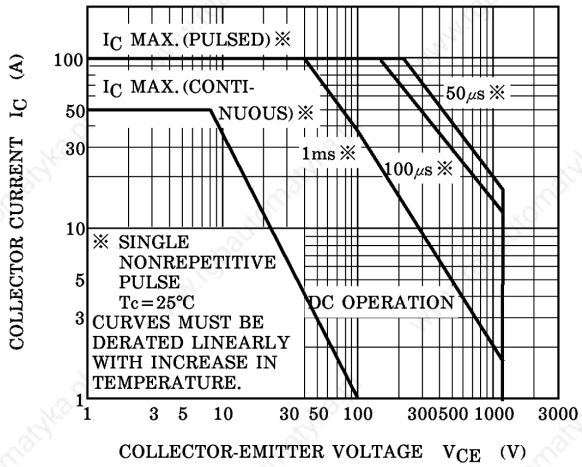
C – V_{CE}



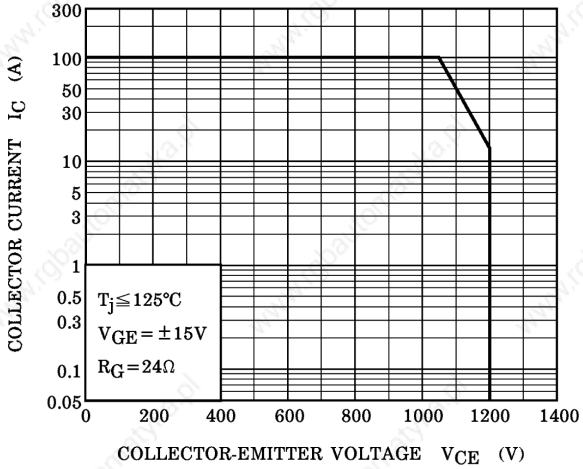
$R_{th(t)}$ – t_w



SAFE OPERATING AREA



REVERSE BIAS SOA



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