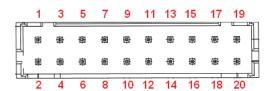
Pin Assignment								
Signals	Name Description	Pin Number(s)						
+Vout	Power rail	SK4						
GND	Power GND	SK5						
Signals	Name Description	SK2 Pin Number						
A2	EEPROM Address	1						
-VPROG	Return connection of external supply for Margin Programming	2						
A1	EEPROM Address	3						
-Vsense	Remote Sense Return	4						
ISHARE	Load share voltage	5						
AO	EEPROM Address	6						
SDA1	Serial Data Signal (I2C)	7						
+VPROG	Positive connection of external supply for Margin Programming	8						
SCL1	Serial Clock Signal (I2C)	9						
+Vsense	Remote Sense Positive	10						
5VSB	5V standby	11						
GND	5V standby Return	12						
5VSB	5V standby	13						
G_DCOK_C	Global DCOK Collector	14						
GPIOA6	EEPROM Write Protect	15						
G_DCOK_E	Global DCOK Emitter (GND)	16						
GND	Return Ground for output signal and I2C communication	17						
G_ACOK_C	Global ACOK Collector	18						
INH_EN	Turn Off Main Output	19						
G_ACOK_E	Global ACOK Emitter (GND)	20						

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Note: Mating connector for SK2 is:

LANDWIN: PN 2050S2000 Housing and PN 2053T021V Contact CIVILUX: PN CI0120SD000 Housing and PN CI01TD21PE0 Contact JST: PN PHDR-20VS housing and PN: SPHD-001T-P0.5



Signal Output Signal Connectors (SK2)
SK2 Mating Connector: JST Part Number PHDR-20VS;
Contact Pins: JST Part Number SPHD-001T-P0.5

## **LED INDICATORS**

Two (2) provided are clearly visible up to a 45 degree offset from vertical with office environment ambient lighting. The status is reflected in the indicator color.

The DC OK LED LED is bicolor. It shall light green if the DC output is within specification, and amber if the output falls out of specification.

**The AC\_OK LED** LED is green if the AC is within specification and off when out of specification. Note: With 5 V standby, Amber also indicates that PSU is in standby mode/output off.

## **CONTROL SIGNALS**

AC\_OK Open collector 0.5 V maximum at 10 mA. Both emitter and collector access provided.

**DC\_OK** Open collector 0.5 V maximum at 10 mA. Both emitter and collector access provided.

DC\_OK will de-assert when output is loss due to OCP, OVP, OTP, or Fan Fault (for -N option).

PS\_INHIBIT/ENABLE Signal 0.0 - 0.5 V contact closure, output OFF

Ordering Information											
LCMXXXY		-	А	-	В	-	С	-	###		
Case Size			Input Termination		Acoustic Noise		Option Codes		Hardware Code		
1-Phase input where XXXX =											
600 = 2.4" x 4.5" x 7.5", 600 W			Blank = IEC connector		Blank = Standard		Blank = No Options		Factory Assigned for Modiefied standards		
			T = Terminal Block		N = Low Noise Fan		1 = Conformal Coat				
Voltage Code Y =							4 = 5 V Standby				
Code							5 = Opt 1 + 4				
L	12										
N	15										
Q	24										
U	36										
W	48										

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## **Mechanical Drawings**

