

# SPECIFICATION

Device Name : Power Integrated Module

Type Name : 7MBR50SB120-50 (RoHS compliant product)

Spec. No. : MS6M 0872

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	DATE	NAME	APPROVED	Fuji Electric Device Technology Co., Ltd.		
DRAWN	Apr. -26 -'05	K.Muramatsu	T. Hosen	DWG.NO.	MS6M0872	1 /
CHECKED	Apr. -26 -'05	S.Ogawa				a
CHECKED	- -	K.Yamada				16

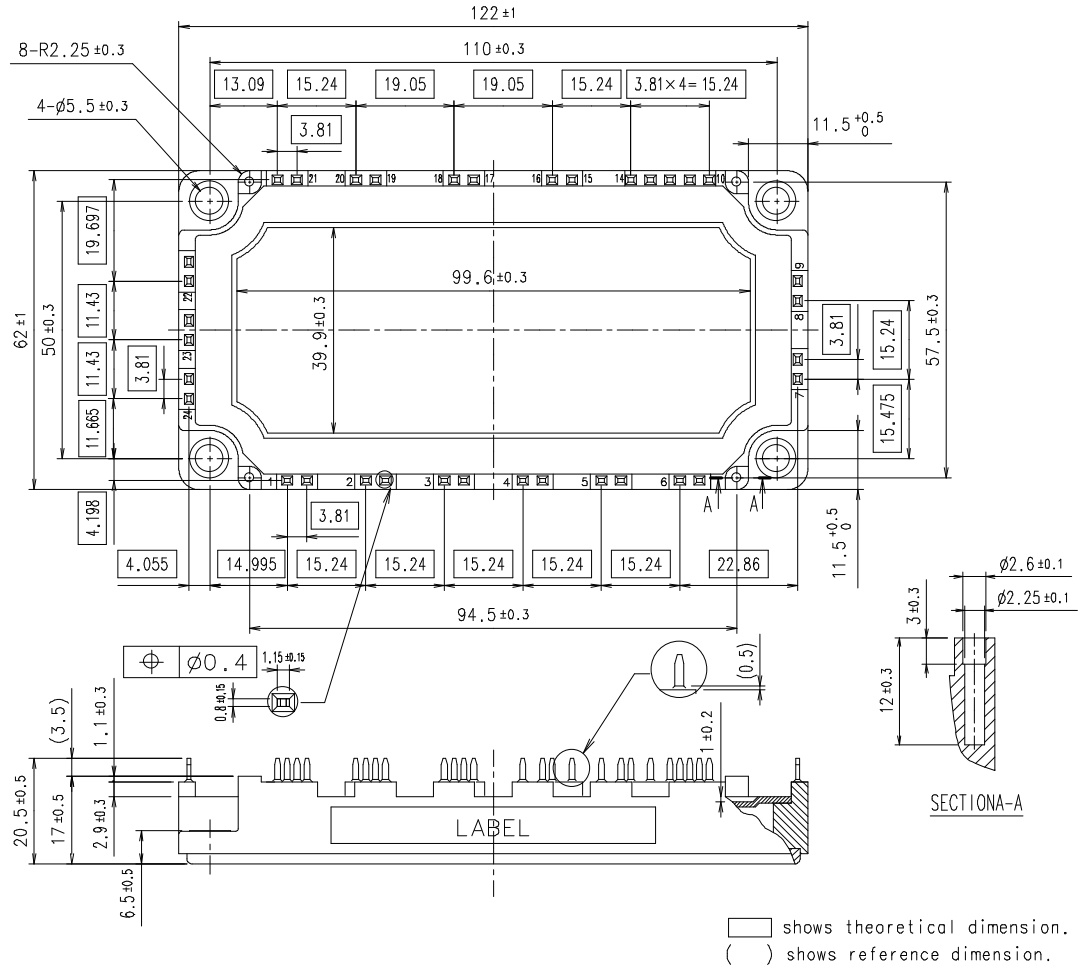
# Revised Records

Date	Classification	Ind.	Content	Applied date	Drawn	Checked	Checked	Approved
Apr.-26-'05	Enactment	—	—————	July-2005	—	S.Ogawa	K.Yamada	T.Hosen
Jul.-14-'05	Revision	a	Revised outline drawing(3/16p) Add packing and labeling(6/16p) Add total weight of soldering material(7/16p) Revised RoHS Directive Compliance(7/16p)		K.Muramatsu	T.Miyasaka	K.Yamada	Y.Seki

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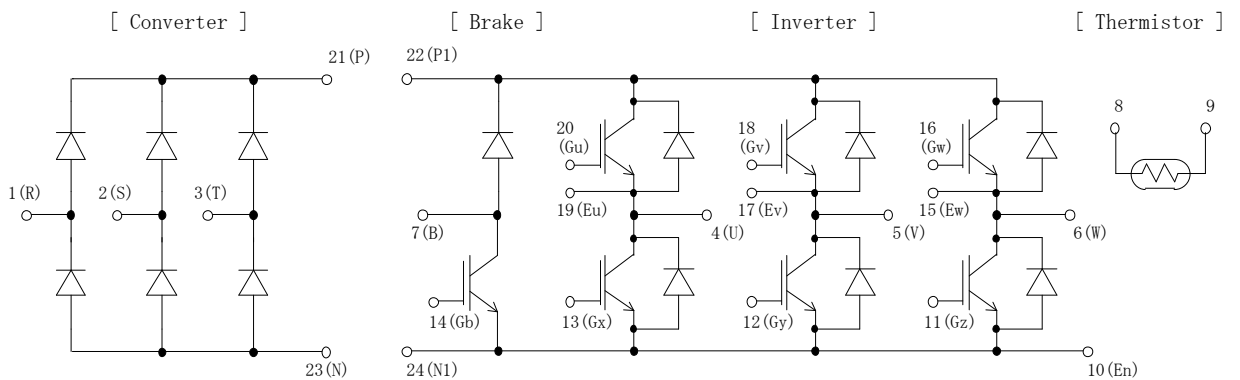
7 M B R 5 0 S B 1 2 0 - 5 0 (RoHS compliant product)

1. Outline Drawing ( Unit : mm )<sup>a</sup>



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2. Equivalent circuit



3. Absolute Maximum Ratings ( at Tc= 25°C unless otherwise specified )

Items		Symbols	Conditions	Maximum Ratings	Units	
Inverter	Collector-Emitter voltage	VCES		1200	V	
	Gate-Emitter voltage	VGES		±20	V	
	Collector current	Ic	Continuous	Tc=25°C	75	A
				Tc=80°C	50	
		Icp	1ms	Tc=25°C	150	A
				Tc=80°C	100	
-Ic			50	A		
Collector Power Dissipation	Pc	1 device		360	W	
Brake	Collector-Emitter voltage	VCES		1200	V	
	Gate-Emitter voltage	VGES		±20	V	
	Collector current	Ic	Continuous	Tc=25°C	35	A
				Tc=80°C	25	
		Icp	1ms	Tc=25°C	70	A
				Tc=80°C	50	
Collector Power Dissipation	Pc	1 device		180	W	
Repetitive peak reverse Voltage(Diode)	VRRM			1200	V	
Converter	Repetitive peak reverse Voltage	VRRM		1600	V	
	Average Output Current	Io	50Hz/60Hz sine wave	50	A	
	Surge Current (Non-Repetitive)	IFSM	Tj=150°C,10ms	520	A	
	I <sup>2</sup> t (Non-Repetitive)	I <sup>2</sup> t	half sine wave	1352	A <sup>2</sup> s	
	Junction temperature	Tj		150	°C	
Storage temperature	Tstg		-40 ~ +125	°C		
Isolation voltage	between terminal and copper base <sup>(*)1</sup>	Viso	AC : 1min.	2500	V	
	between thermistor and others <sup>(*)2</sup>			2500		
Mounting Screw Torque <sup>(*)3</sup>				3.5	N · m	

(\*)1 All terminals should be connected together when isolation test will be done.

(\*)2 Terminal 8 and 9 should be connected together. Terminal 1 to 7 and 10 to 24 should be connected together and shorted to copper base.

(\*)3 Recommendable Value : 2.5~3.5 N · m (M5)

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4. Electrical characteristics ( at Tj= 25°C unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units		
			min.	typ.	Max.			
Inverter	Zero gate voltage Collector current	ICES	VGE = 0 V, VCE = 1200 V	-	-	1.0	mA	
	Gate-Emitter leakage current	IGES	VCE = 0 V, VGE = ±20 V	-	-	200	nA	
	Gate-Emitter threshold voltage	VGE(th)	VCE = 20 V, Ic = 50 mA	5.5	7.2	8.5	V	
	Collector-Emitter saturation voltage	VCE(sat)	VGE = 15 V, Ic = 50 A	chip	-	2.1	-	V
				terminal	-	2.3	2.7	
	Input capacitance	Cies	VGE = 0 V, VCE = 10 V f = 1 MHz	-	6000	-	pF	
	Turn-on time	ton	Vcc= 600 V	-	0.35	1.2	μs	
		tr	Ic = 50 A	-	0.25	0.6		
		tr(i)	VGE = ±15 V	-	0.1	-		
	Turn-off time	toff	RG = 24 Ω	-	0.45	1.0	μs	
tf			-	0.08	0.3			
Forward on voltage	VF	IF = 50 A	chip	-	2.3	-	V	
			terminal	-	2.5	3.3		
Reverse recovery time	trr	IF = 50 A	-	-	350	ns		
Brake	Zero gate voltage Collector current	ICES	VGE = 0 V, VCE = 1200 V	-	-	1.0	mA	
	Gate-Emitter leakage current	IGES	VCE = 0 V, VGE = ±20 V	-	-	200	nA	
	Collector-Emitter saturation voltage	VCE(sat)	VGE = 15 V, Ic = 25 A	chip	-	2.1	-	V
				terminal	-	2.25	2.7	
	Turn-on time	ton	Vcc= 600 V	-	0.35	1.2	μs	
		tr	Ic = 25 A	-	0.25	0.6		
	Turn-off time	toff	VGE = ±15 V	-	0.45	1.0	μs	
tf		RG = 51 Ω	-	0.08	0.3			
Reverse current	IRRM	VR = 1200 V	-	-	1.0	mA		
Converter	Forward on voltage	VFM	IF = 50 A	chip	-	1.1	-	V
				terminal	-	1.2	1.5	
Reverse current	IRRM	VR = 1600 V	-	-	1.0	mA		
Thermistor	Resistance	R	T = 25°C	-	5000	-	Ω	
			T = 100°C	465	495	520		
B value	B	T = 25/50°C	3305	3375	3450	K		

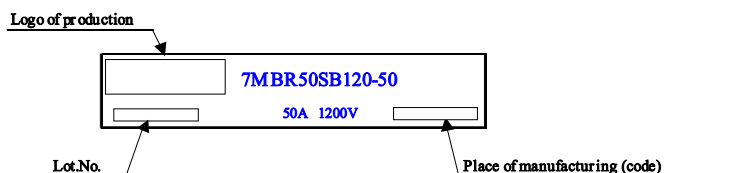
5. Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	Max.	
Thermal resistance (1 device)	Rth(j-c)	Inverter IGBT	-	-	0.35	°C/W
		Inverter FWD	-	-	0.75	
		Brake IGBT	-	-	0.69	
		Converter Diode	-	-	0.50	
Contact Thermal resistance	Rth(c-f)	with Thermal Compound (※)	-	0.05	-	°C/W

※ This is the value which is defined mounting on the additional cooling fin with thermal compound.

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### 6. Indication on module (モジュール表示)



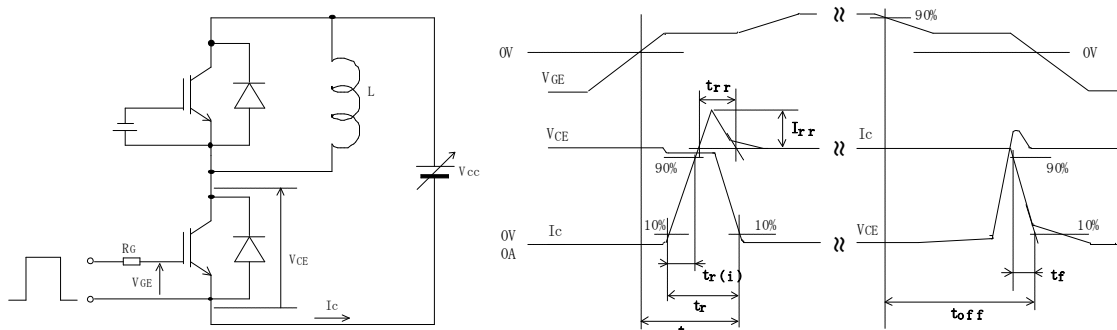
### 7. Applicable category (適用範囲)

This specification is applied to Power Integrated Module named 7MBR50SB120-50 .  
 本納入仕様書はパワー集積モジュール 7 M B R 5 0 S B 1 2 0 - 5 0 に適用する。

### 8. Storage and transportation notes (保管・運搬上の注意事項)

- The module should be stored at a standard temperature of 5 to 35°C and humidity of 45 to 75% .  
 Be careful to solderability of the terminals if the module has passed over one year from manufacturing date, under the above storage condition.  
 常温・常湿保存が望ましい。( 5~35°C, 45~75%)  
 本保存条件下で、製造から1年以上経過した場合は端子はんだ付け性に十分注意すること。
- Store modules in a place with few temperature changes in order to avoid condensation on the module surface.  
 急激な温度変化のなきこと。(モジュール表面が結露しないこと)
- Avoid exposure to corrosive gases and dust.  
 腐蝕性ガスの発生場所、塵埃の多い場所は避けること。
- Avoid excessive external force on the module.  
 製品に荷重がかからないように十分注意すること。
- Store modules with unprocessed terminals.  
 モジュールの端子は未加工の状態での保管すること。
- Do not drop or otherwise shock the modules when transporting.  
 製品の運搬時に衝撃を与えたり、落下させたりしないこと。

### 9. Definitions of switching time (スイッチング時間の定義)

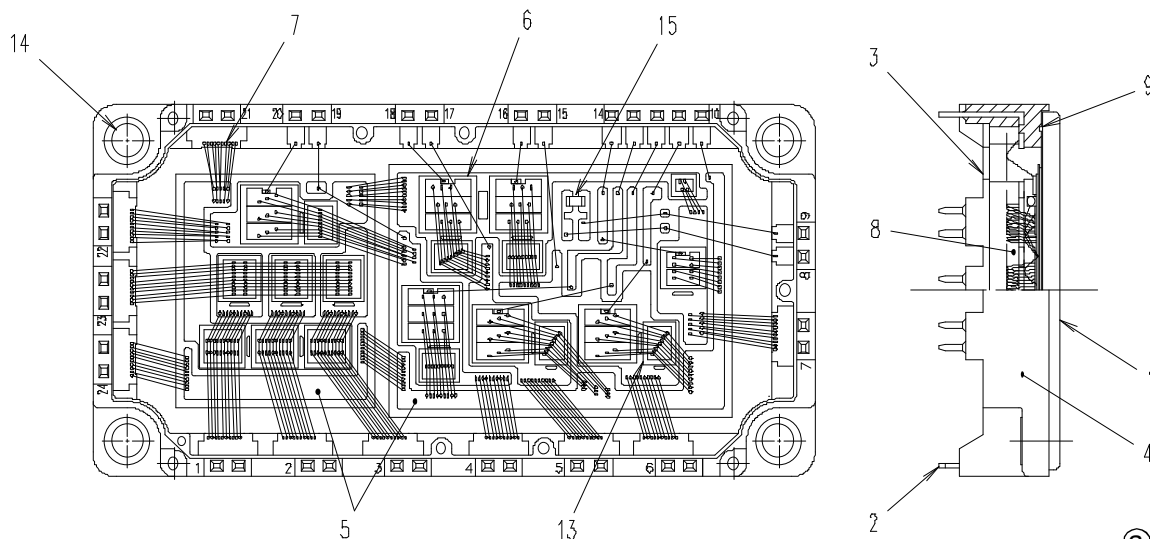


### 10. Packing and Labeling (梱包仕様) <sup>Ⓐ</sup>

- Display on the packing box
- Logo of production
  - Type name
  - Lot No
  - Products quantity in a packing box

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### 10. List of material (材料リスト)



(Total weight of soldering material(typ): 8.8 g)<sup>①</sup>

No.	Parts	Material (main)	Ref.
1	Base Plate	Cu	Ni plating
2	Terminal	Cu	Ni plating (Internal) Lead free solder plating (External)
3	Cover	PPS resin	UL 94V-0
4	Case	PPS resin	UL 94V-0
5	Isolation substrate	Al <sub>2</sub> O <sub>3</sub> + Cu	
6	IGBT chip	Silicon	
7	Wiring	Aluminum	
8	Silicone Gel	Silicone resin	
9	Adhesive	Silicone resin	
10	Solder (Under chip)	Sn/Ag base	(Not drawn in above)
11	Solder (Under Isolation substrate)	Sn/Ag base	(Not drawn in above)
12	Label	Paper	(Not drawn in above)
13	FWD chip	Silicon	
14	Ring	Fe	Trivalent Chromate treatment
15	Thermistor	Lead glass	

### 12. RoHS Directive Compliance (RoHS 指令適用について)<sup>①</sup>

本PIMは富士電機デバイステクノロジーが発行しているRoHSに関する資料MS5F6209を適用する。日本語版(MS5F6212)は参考資料とする。

The document (MS5F6209) about RoHS that Fuji Electric Device Technology issued is applied to this Power Integrated Module. The Japanese Edition(MS5F6212) is made into a reference grade.

13. Reliability test results

**Reliability Test Items**

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of sample	Acceptance number
Mechanical Tests	1 Terminal Strength (Pull test)	Pull force : 20N Test time : 10±1 sec.	Test Method 401 Method I	5	(0 : 1)
	2 Mounting Strength	Screw torque : 2.5 ~ 3.5 N·m (M5) Test time : 10±1 sec.	Test Method 402 method II	5	(0 : 1)
	3 Vibration	Range of frequency : 10 ~ 500Hz Sweeping time : 15 min. Acceleration : 100m/s <sup>2</sup> Sweeping direction : Each X,Y,Z axis Test time : 6 hr. (2hr./direction)	Test Method 403 Reference 1 Condition code B	5	(0 : 1)
	4 Shock	Maximum acceleration : 5000m/s <sup>2</sup> Pulse width : 1.0msec. Direction : Each X,Y,Z axis Test time : 3 times/direction	Test Method 404 Condition code B	5	(0 : 1)
	5 Solderability	Solder temp. : 245±5 °C Immersion time : 5±0.5sec. Test time : 1 time Solder Alloy : Sn-Ag-Cu Each terminal should be Immersed in solder within 1~1.5mm from the body.	Test Method 303	5	(0 : 1)
	6 Resistance to Soldering Heat	Solder temp. : 260±5 °C Immersion time : 10±1sec. Test time : 1 time Each terminal should be Immersed in solder within 1~1.5mm from the body.	Test Method 302 Condition code A	5	(0 : 1)
Environment Tests	1 High Temperature Storage	Storage temp. : 125±5 °C Test duration : 1000hr.	Test Method 201	5	(0 : 1)
	2 Low Temperature Storage	Storage temp. : -40±5 °C Test duration : 1000hr.	Test Method 202	5	(0 : 1)
	3 Temperature Humidity Storage	Storage temp. : 85±2 °C Relative humidity : 85±5% Test duration : 1000hr.	Test Method 103 Test code C	5	(0 : 1)
	4 Unsaturated Pressurized Vapor	Test temp. : 120±2 °C Test humidity : 85±5% Test duration : 96hr.	Test Method 103 Test code E	5	(0 : 1)
	5 Temperature Cycle	Test temp. : Low temp. -40±5 °C High temp. 125 ±5 °C RT 5 ~ 35 °C Dwell time : High ~ RT ~ Low ~ RT 1hr. 0.5hr. 1hr. 0.5hr. Number of cycles : 100 cycles	Test Method 105	5	(0 : 1)
	6 Thermal Shock	Test temp. : High temp. 100 <sup>+0</sup> °C Low temp. 0 <sup>+5</sup> °C Used liquid : Water with ice and boiling water Dipping time : 5 min. par each temp. Transfer time : 10 sec. Number of cycles : 10 cycles	Test Method 307 method I Condition code A	5	(0 : 1)

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## Reliability Test Items

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of sample	Acceptance number
Endurance Tests	1 High temperature Reverse Bias (for Collector-Emitter)	Test temp. : $T_a = 125 \pm 5 \text{ }^\circ\text{C}$ ( $T_j \leq 150 \text{ }^\circ\text{C}$ ) Bias Voltage : $V_C = 0.8 \times V_{CES}$ Bias Method : Applied DC voltage to C-E $V_{GE} = 0V$ Test duration : 1000hr.	Test Method 101	5	(0 : 1)
	2 High temperature Bias (for gate)	Test temp. : $T_a = 125 \pm 5 \text{ }^\circ\text{C}$ ( $T_j \leq 150 \text{ }^\circ\text{C}$ ) Bias Voltage : $V_C = V_{GE} = +20V$ or $-20V$ Bias Method : Applied DC voltage to G-E $V_{CE} = 0V$ Test duration : 1000hr.	Test Method 101	5	(0 : 1)
	3 High temperature Reverse Bias (for Cathode Anode)	Test temp. : $T_a = 125 \pm 5 \text{ }^\circ\text{C}$ ( $T_j \leq 150 \text{ }^\circ\text{C}$ ) Bias Voltage : $V_C = 0.8 \times V_{RRM}$ Bias Method : Applied AC half-wave voltage to K-A Test duration : 1000hr.	Test Method 101	5	(0 : 1)
	4 Intermitted Operating Life (Power cycle) (for IGBT)	ON time : 2 sec. OFF time : 18 sec. Test temp. : $\Delta T_j = 100 \pm 5 \text{ deg}$ $T_j \leq 150 \text{ }^\circ\text{C}$ , $T_a = 25 \pm 5 \text{ }^\circ\text{C}$ Number of cycles : 15000 cycles	Test Method 106	5	(0 : 1)

## Failure Criteria

Item	Characteristic	Symbol	Failure criteria		Unit	Note
			Lower limit	Upper limit		
Electrical characteristic	Leakage current	ICES	-	USL×2	mA	
		±IGES	-	USL×2	μA	
	Gate threshold voltage	VGE(th)	LSL×0.8	USL×1.2	mA	
	Saturation voltage	VCE(sat)	-	USL×1.2	V	
	Forward voltage	VF	-	USL×1.2	V	
	Thermal resistance	IGBT	Δ VGE or Δ VCE	-	USL×1.2	mV
FWD			Δ VF	-	USL×1.2	mV
	Isolation voltage	Viso	Broken insulation		-	
Visual inspection	Visual inspection					
	Peeling Plating and the others	-	The visual sample		-	

LSL : Lower specified limit.

USL : Upper specified limit.

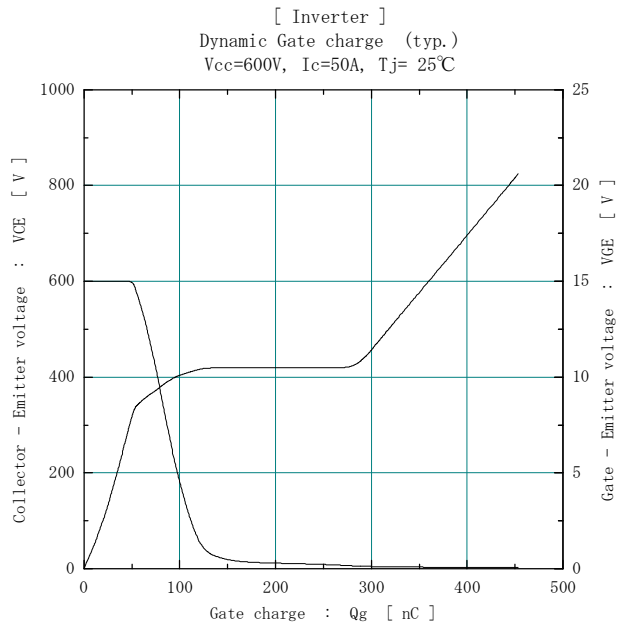
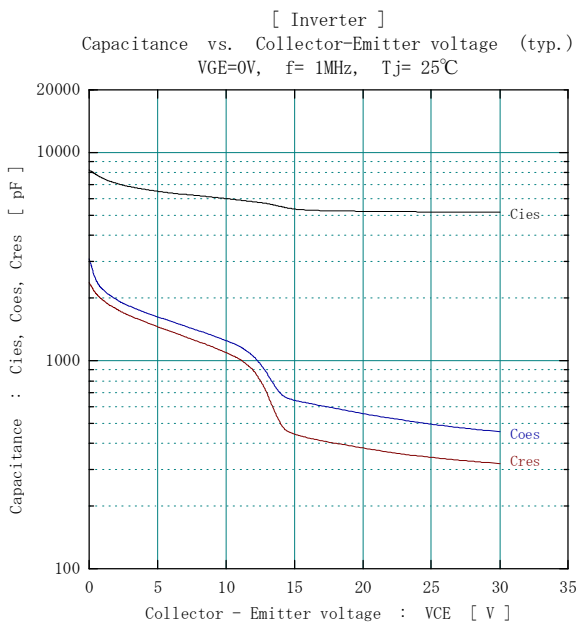
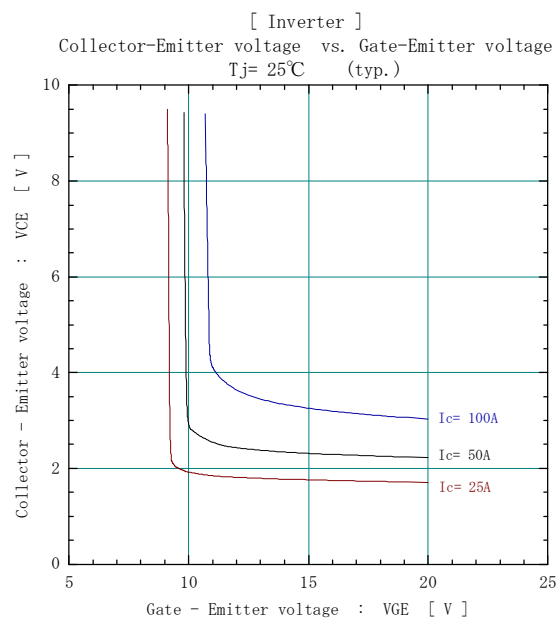
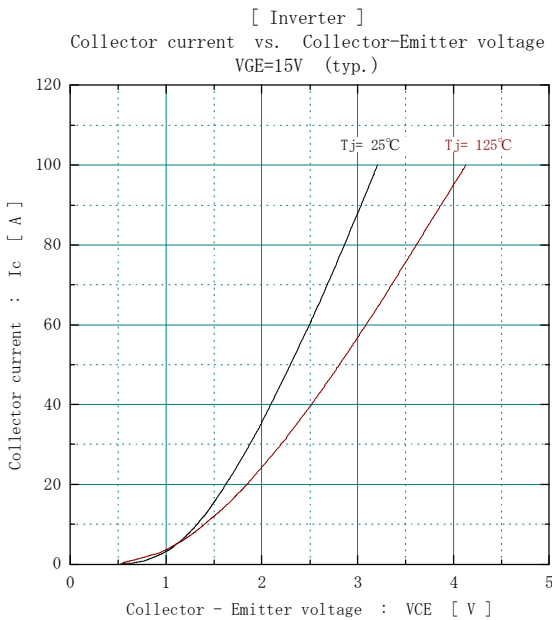
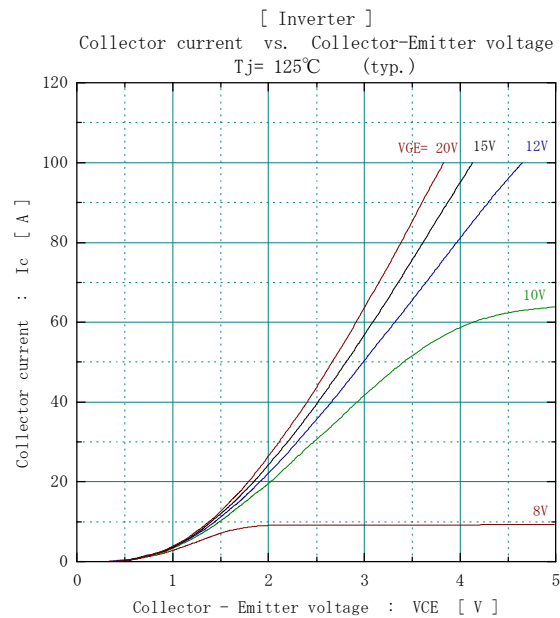
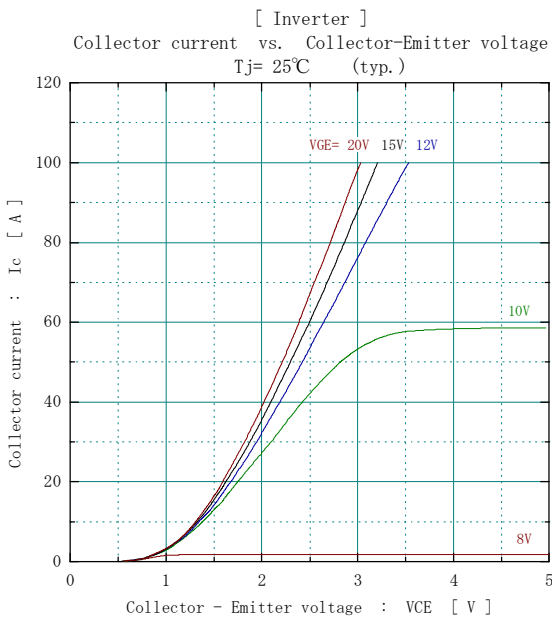
Note : Each parameter measurement read-outs shall be made after stabilizing the components at room ambient for 2 hours minimum, 24 hours maximum after removal from the tests. And in case of the wetting tests, for example, moisture resistance tests, each component shall be made wipe or dry completely before the measurement.

## Reliability Test Results

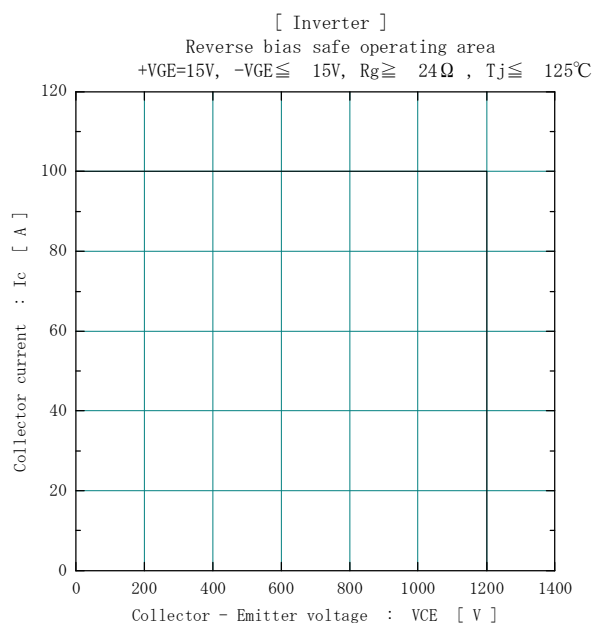
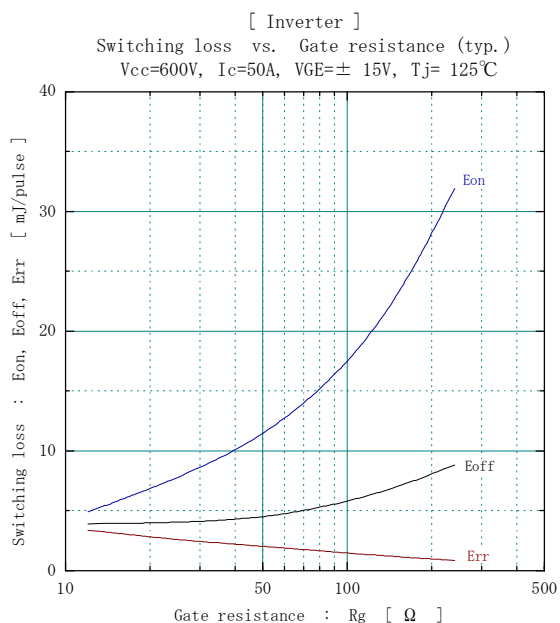
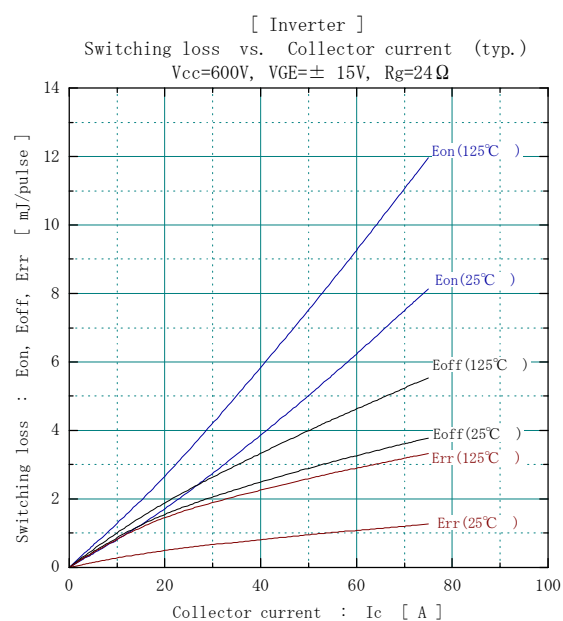
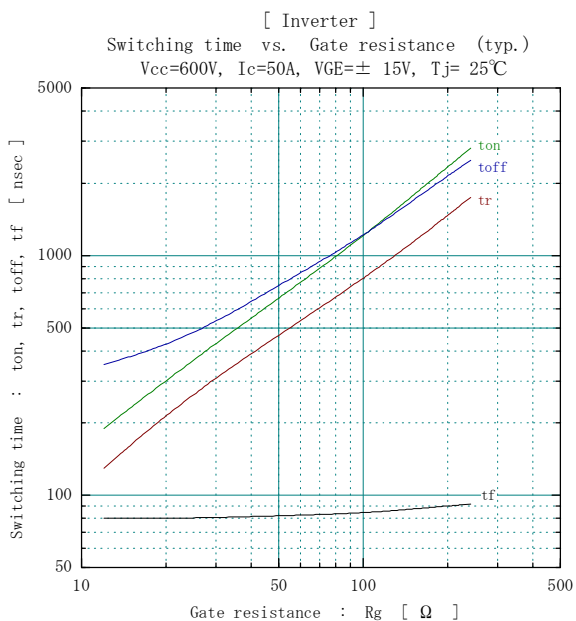
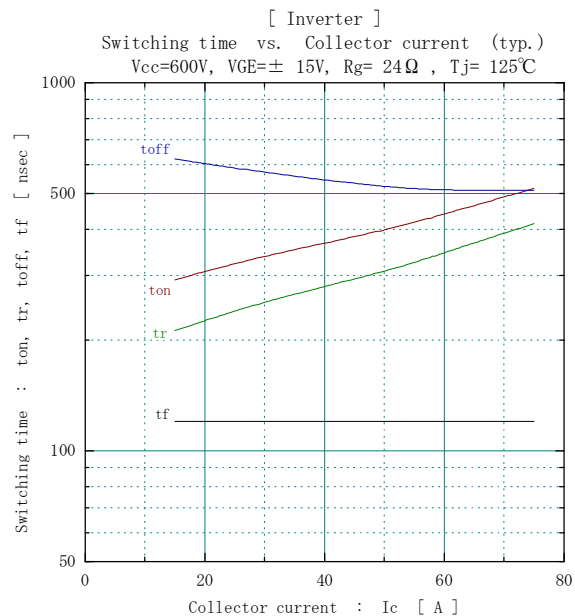
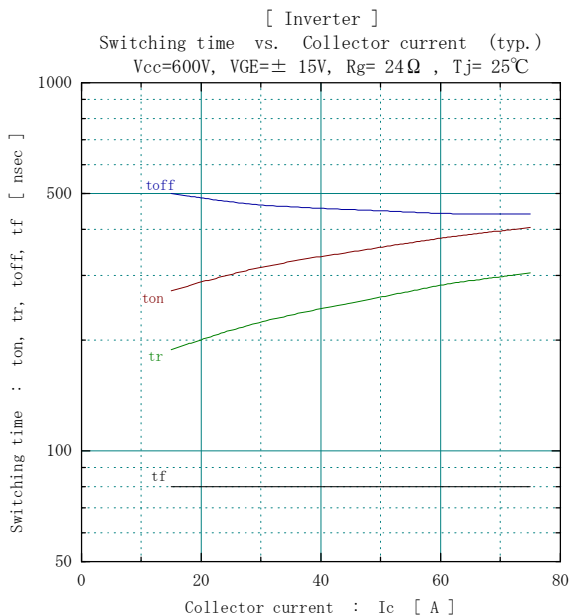
Test categories	Test items	Reference norms EIAJ ED-4701 (Aug.-2001 edition)	Number of test sample	Number of failure sample
Mechanical Tests	1 Terminal Strength (Pull test)	Test Method 401 Method I	5	0
	2 Mounting Strength	Test Method 402 method II	5	0
	3 Vibration	Test Method 403 Condition code B	5	0
	4 Shock	Test Method 404 Condition code B	5	0
	5 Solderability	Test Method 303	5	0
	6 Resistance to Soldering Heat	Test Method 302 Condition code A	5	0
Environment Tests	1 High Temperature Storage	Test Method 201	5	0
	2 Low Temperature Storage	Test Method 202	5	0
	3 Temperature Humidity Storage	Test Method 103 Test code C	5	0
	4 Unsaturated Pressurized Vapor	Test Method 103 Test code E	5	0
	5 Temperature Cycle	Test Method 105	5	0
	6 Thermal Shock	Test Method 307 method I Condition code A	5	0
Endurance Tests	1 High temperature Reverse Bias ( for Collector-Emitter)	Test Method 101	5	0
	2 High temperature Bias ( for gate )	Test Method 101	5	0
	3 High temperature Reverse Bias ( for Cathode Anode)	Test Method 101	5	0
	4 Intermitted Operating Life (Power cycling) ( for IGBT )	Test Method 106	5	0

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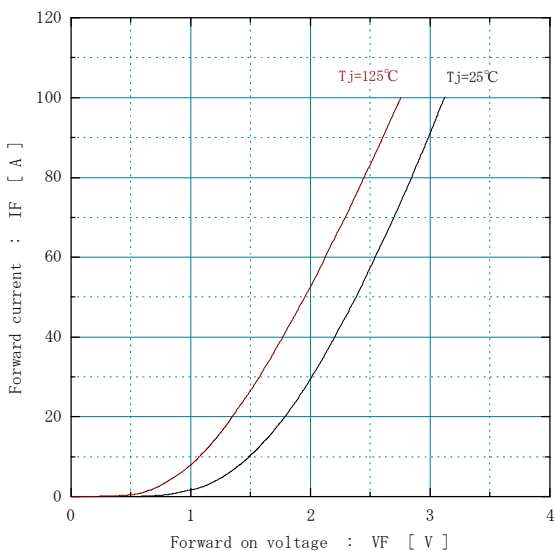


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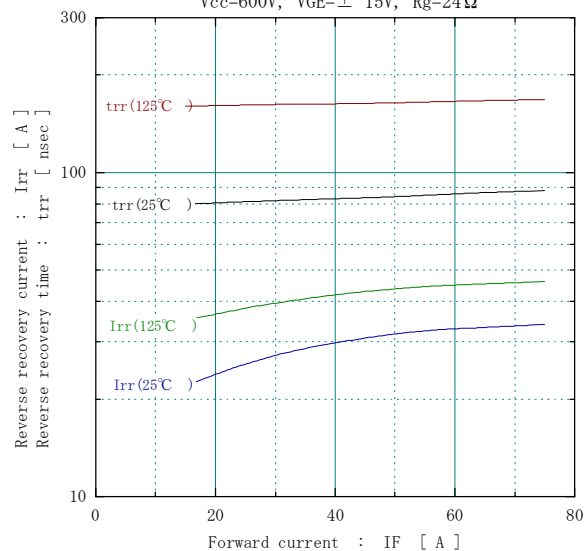


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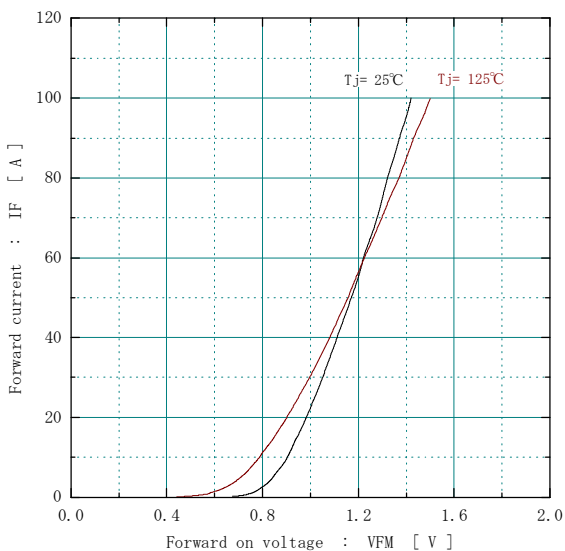
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Forward current vs. Forward on voltage (typ.)



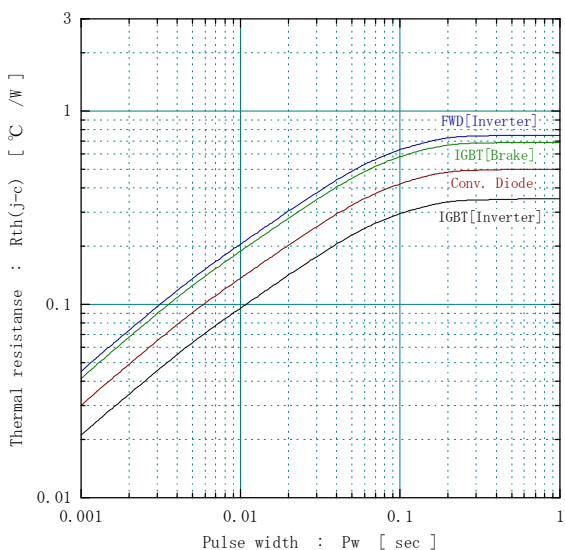
[ Inverter ]  
Reverse recovery characteristics (typ.)  
Vcc=600V, VGE=± 15V, Rg=24Ω



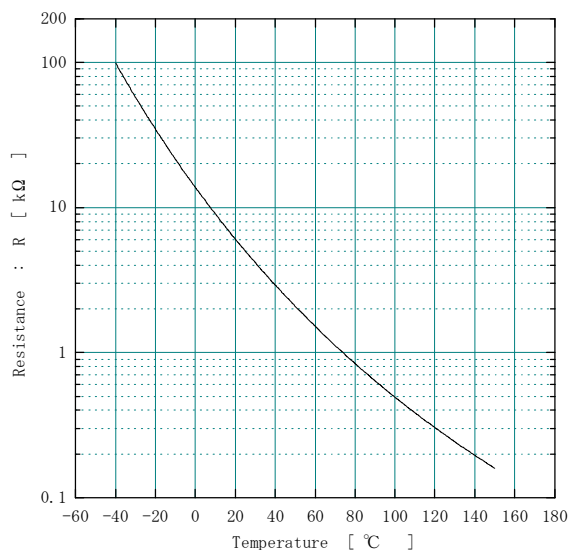
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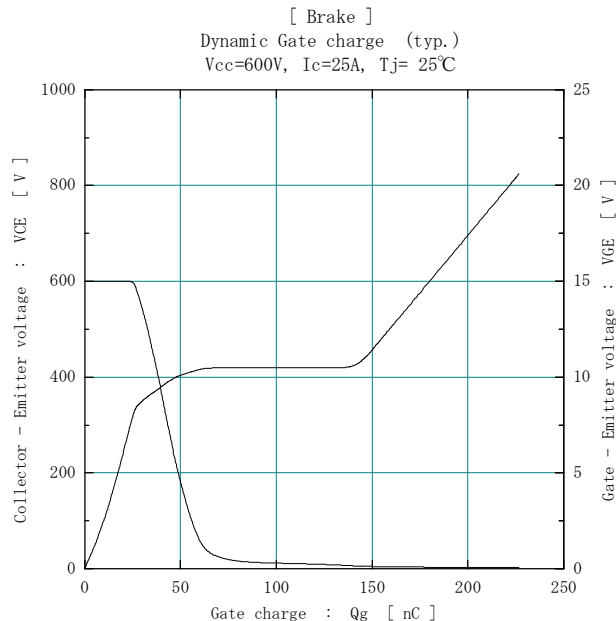
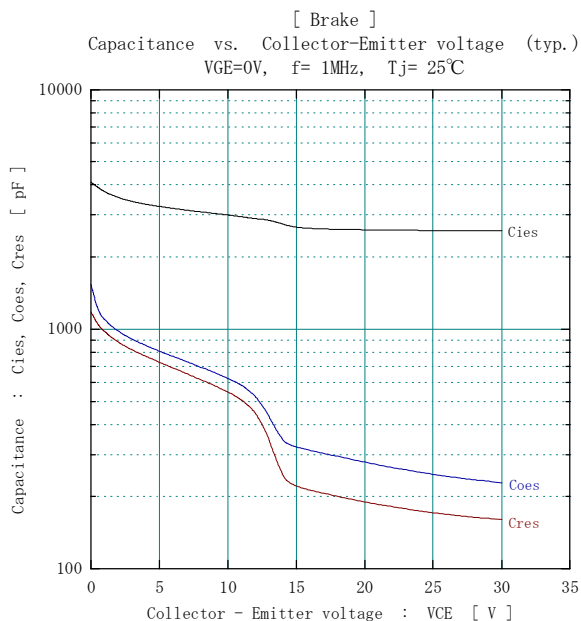
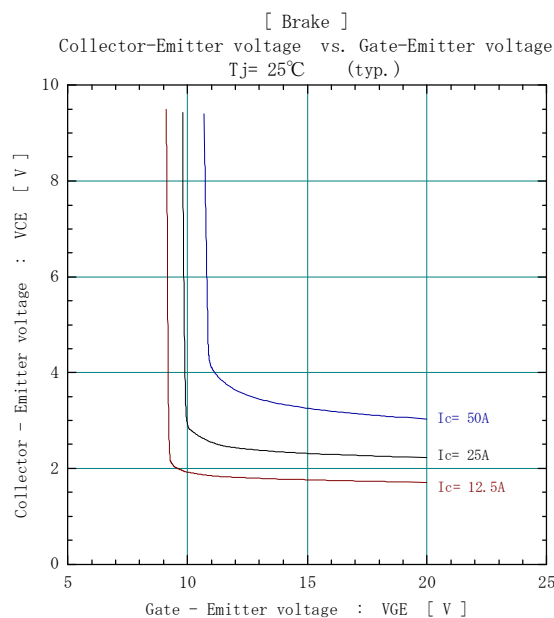
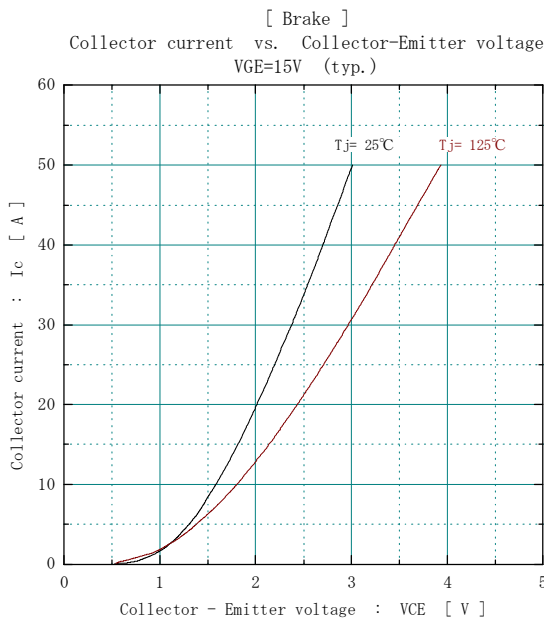
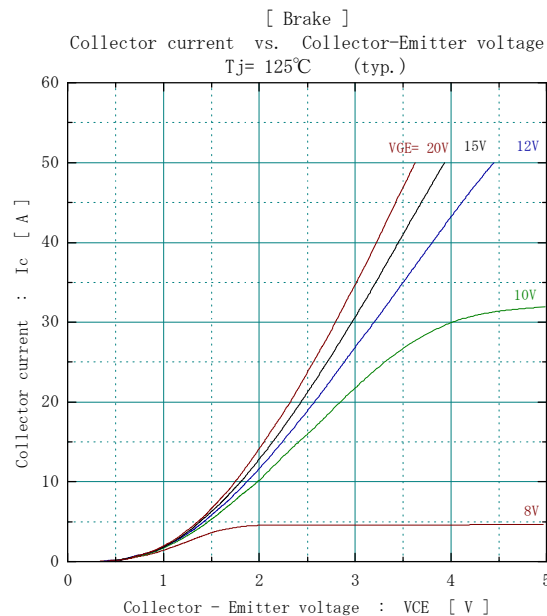
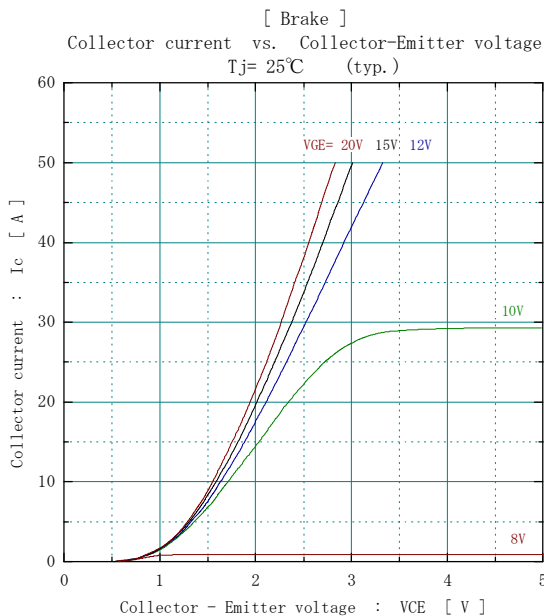
Transient thermal resistance



[ Thermistor ]  
Temperature characteristic (typ.)



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## Warnings

- This product shall be used within its absolute maximum rating (voltage, current, and temperature). This product may be broken in case of using beyond the ratings.  
製品の絶対最大定格(電圧, 電流, 温度等)の範囲内で御使用下さい。絶対最大定格を超えて使用すると、素子が破壊する場合があります。
- Connect adequate fuse or protector of circuit between three-phase line and this product to prevent the equipment from causing secondary destruction, such as fire, its spreading, or explosion.  
万一の不慮の事故で素子が破壊した場合を考慮し、商用電源と本製品の間に適切な容量のヒューズ又はブレーカーを必ず付けて火災, 爆発, 延焼等の2次破壊を防いでください。
- Use this product after realizing enough working on environment and considering of product's reliability life. This product may be broken before target life of the system in case of using beyond the product's reliability life.  
製品の使用環境を十分に把握し、製品の信頼性寿命が満足できるか検討の上、本製品を適用して下さい。製品の信頼性寿命を超えて使用した場合、装置の目標寿命より前に素子が破壊する場合があります。
- When electric power is connected to equipments, rush current will be flown through rectifying diode to charge DC capacitor. Guaranteed value of the rush current is specified as  $I^2t$  (non-repetitive), however frequent rush current through the diode might make it's power cycle destruction occur because of the repetitive power. In application which has such frequent rush current, well consideration to product life time (i.e. suppressing the rush current) is necessary.  
電源投入時に整流用ダイオードには、コンデンサーを充電する為の突入電流が流れます。この突入電流に対する保証値は  $I^2t$ (非繰返し)として表記されていますが、この突入電流が頻繁に流れると  $I^2t$ 破壊とは別に整流用ダイオードの繰返し負荷によるパワーサイクル耐量破壊を起こす可能性があります。突入電流が頻繁に流れるようなアプリケーションでは、突入電流値を抑えるなど、製品寿命に十分留意してご使用下さい。
- If the product had been used in the environment with acid, organic matter, and corrosive gas ( hydrogen sulfide, sulfurous acid gas), the product's performance and appearance can not be ensured easily.  
酸・有機物・腐食性ガス(硫化水素, 亜硫酸ガス等)を含む環境下で使用された場合、製品機能・外観等の保証はできません。
- Use this product within the power cycle curve (Technical Rep.No. : MT6M3947). Power cycle capability is classified to delta-Tj mode which is stated as above and delta-Tc mode. Delta-Tc mode is due to rise and down of case temperature (Tc), and depends on cooling design of equipment which use this product. In application which has such frequent rise and down of Tc, well consideration of product life time is necessary.  
本製品は、パワーサイクル寿命カーブ以下で使用下さい(技術資料No.: MT6M3947)。パワーサイクル耐量にはこの  $\Delta Tj$  による場合の他に、 $\Delta Tc$  による場合があります。これはケース温度( $Tc$ )の上昇下降による熱ストレスであり、本製品をご使用する際の放熱設計に依存します。ケース温度の上昇下降が頻繁に起こる場合は、製品寿命に十分留意してご使用下さい。
- Never add mechanical stress to deform the main or control terminal. The deformed terminal may cause poor contact problem.  
主端子及び制御端子に応力を与えて変形させないで下さい。端子の変形により、接触不良などを引き起こす場合があります。
- Use this product with keeping the cooling fin's flatness between screw holes within 100um at 100mm and the roughness within 10um. Also keep the tightening torque within the limits of this specification. Too large convex of cooling fin may cause isolation breakdown and this may lead to a critical accident. On the other hand, too large concave of cooling fin makes gap between this product and the fin bigger, then, thermal conductivity will be worse and over heat destruction may occur.  
冷却フィンにはネジ取り付け位置間で平坦度を100mmで100um以下、表面の粗さは10um以下にして下さい。過大な凸反りがあったりすると本製品が絶縁破壊を起こし、重大事故に発展する場合があります。また、過大な凹反りやゆがみ等があると、本製品と冷却フィンの間有空隙が生じて放熱が悪くなり、熱破壊に繋がる場合があります。
- In case of mounting this product on cooling fin, use thermal compound to secure thermal conductivity. If the thermal compound amount was not enough or its applying method was not suitable, its spreading will not be enough, then, thermal conductivity will be worse and thermal run away destruction may occur.  
Confirm spreading state of the thermal compound when its applying to this product.  
(Spreading state of the thermal compound can be confirmed by removing this product after mounting.)  
素子を冷却フィンに取り付ける際には、熱伝導を確保するためのコンパウンド等をご使用ください。又、塗布量が不足したり、塗布方法が不適だったりすると、コンパウンドが十分に素子全体に広がらず、放熱悪化による熱破壊に繋がる事があります。コンパウンドを塗布する際には、製品全面にコンパウンドが広がっている事を確認してください。  
(実装した後に素子を取りはずすとコンパウンドの広がり具合を確認する事が出来ます。)
- It shall be confirmed that IGBT's operating locus of the turn-off voltage and current are within the RBSOA specification. This product may be broken if the locus is out of the RBSOA.  
ターンオフ電圧・電流の動作軌跡がRBSOA仕様内にあることを確認して下さい。RBSOAの範囲を超えて使用すると素子が破壊する可能性があります。

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## Warnings

- If excessive static electricity is applied to the control terminals, the devices may be broken. Implement some countermeasures against static electricity.  
制御端子に過大な静電気が印加された場合、素子が破壊する場合があります。取り扱い時は静電気対策を実施して下さい。
- Never add the excessive mechanical stress to the main or control terminals when the product is applied to equipments. The module structure may be broken.  
素子を装置に実装する際に、主端子や制御端子に過大な応力を与えないで下さい。端子構造が破壊する可能性があります。
- In case of insufficient -VGE, erroneous turn-on of IGBT may occur. -VGE shall be set enough value to prevent this malfunction. (Recommended value : -VGE = -15V)  
逆バイアスゲート電圧-VGEが不足しますと誤点弧を起こす可能性があります。誤点弧を起こさない為に-VGEは十分な値で設定して下さい。(推奨値 : -VGE = -15V)
- In case of higher turn-on dv/dt of IGBT, erroneous turn-on of opposite arm IGBT may occur. Use this product in the most suitable drive conditions, such as +VGE, -VGE, RG to prevent the malfunction.  
ターンオン dv/dt が高いと対抗アームのIGBTが誤点弧を起こす可能性があります。誤点弧を起こさない為の最適なドライブ条件(+VGE, -VGE, RG等)でご使用下さい。

## Cautions

- Fuji Electric Device Technology is constantly making every endeavor to improve the product quality and reliability. However, semiconductor products may rarely happen to fail or malfunction. To prevent accidents causing injury or death, damage to property like by fire, and other social damage resulted from a failure or malfunction of the Fuji Electric Device Technology semiconductor products, take some measures to keep safety such as redundant design, spread-fire-preventive design, and malfunction-protective design.  
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- The product described in this specification is not designed nor made for being applied to the equipment or systems used under life-threatening situations. When you consider applying the product of this specification to particular used, such as vehicle-mounted units, shipboard equipment, aerospace equipment, medical devices, atomic control systems and submarine relaying equipment or systems, please apply after confirmation of this product to be satisfied about system construction and required reliability.  
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