

4 Form C 2 coil latching

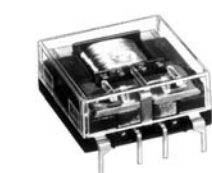
Flat series	Vertical series		Coil voltage, V DC			Coil resistance, Ω (±10%)	Nominal operating power, mW
PC board terminal	Plug-in	PC board terminal	Pick-up voltage (max.)	Reset voltage (max.)	Maximum allowable voltage (within 2 min.)		
NC4D-JPL2-DC5V	NC4D-L2-DC5V	NC4D-PL2-DC5V	4.0	4.0	5.5	15.6	1,600
NC4D-JPL2-DC6V	NC4D-L2-DC6V	NC4D-PL2-DC6V	4.8	4.8	6.6	22.5	
NC4D-JPL2-DC12V	NC4D-L2-DC12V	NC4D-PL2-DC12V	9.6	9.6	13.2	90	
NC4D-JPL2-DC24V	NC4D-L2-DC24V	NC4D-PL2-DC24V	19.2	19.2	26.4	360	
NC4D-JPL2-DC48V	NC4D-L2-DC48V	NC4D-PL2-DC48V	38.4	38.4	52.8	1,440	
NC4D-JPL2-DC110V	NC4D-L2-DC110V	NC4D-PL2-DC110V	88.0	88.0	121	7,560	

Notes:

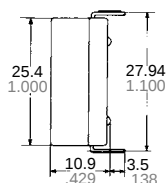
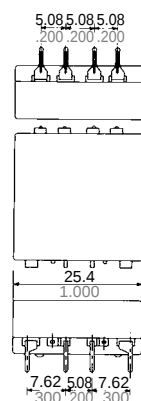
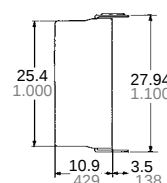
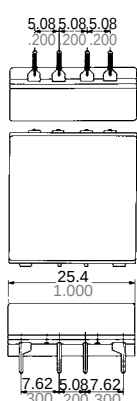
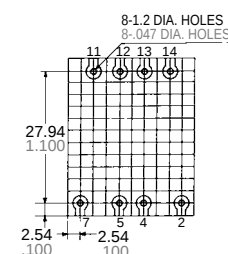
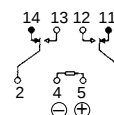
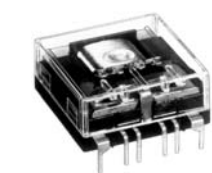
- Two coil latching relay 4C series are for intermittent operation only. Power should be applied to coil continuously for no more than two minutes.
- Coil resistance is the measured value at a coil temperature of 20°C. Compensate coil resistance by plus or minus 0.4% for each degree (°C) of coil temperature change.
- "Maximum allowable voltage" is that value at maximum contact rating and maximum ambient temperature. The graph shown in the data describes the inter-relationship; care should be taken to prevent the total of ambient temperature and the coil temperature rise from exceeding 120°C.

DIMENSIONS

mm inch

Flat series2C single side stable
(NC2D-JP)

(NC2EBD-JP)

**Standard type****Amber sealed type****PC board pattern (Copper-side view)**Tolerance: $\pm 0.1 \pm .004$ **Schematic (Top view)****Deenergized position**General tolerance: $\pm 0.5 \pm .020$ 2C 2 coil latching
(NC2D-JPL2)

(NC2EBD-JPL2)

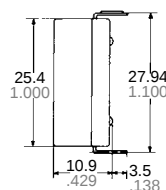
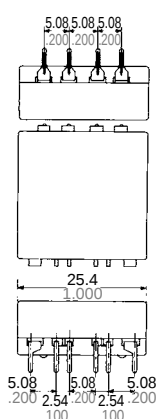
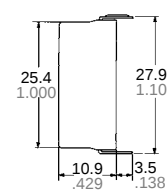
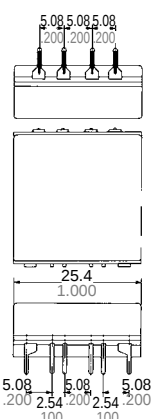
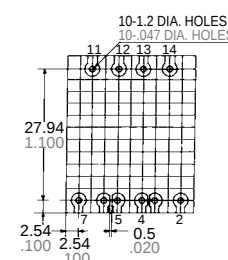
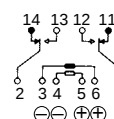
**Standard type****Amber sealed type****PC board pattern (Copper-side view)**Tolerance: $\pm 0.1 \pm .004$ **Schematic (Top view)**

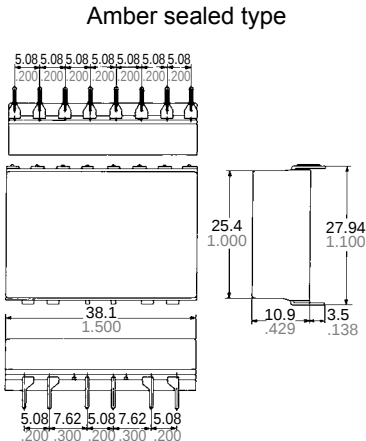
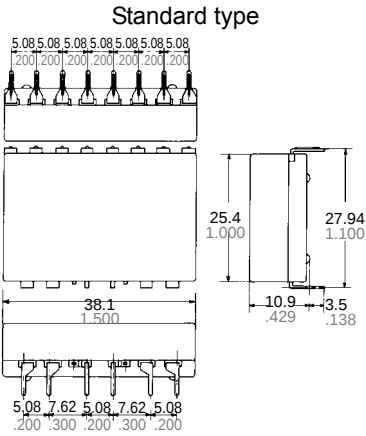
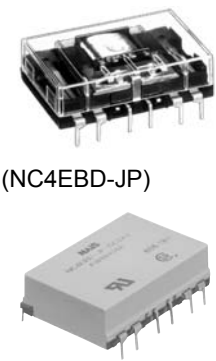
Diagram shows the "reset" position when terminals 3 and 6 are energized. Energize terminals 4 and 5 to transfer contacts.

General tolerance: $\pm 0.5 \pm .020$

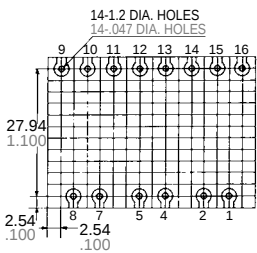
NC

4C single side stable
(NC4D-JP)

mm inch

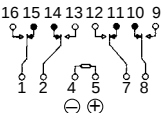


PC board pattern (Copper-side view)



Tolerance: $\pm 0.1 \pm .004$

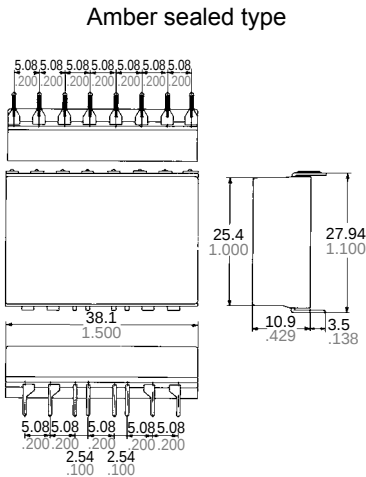
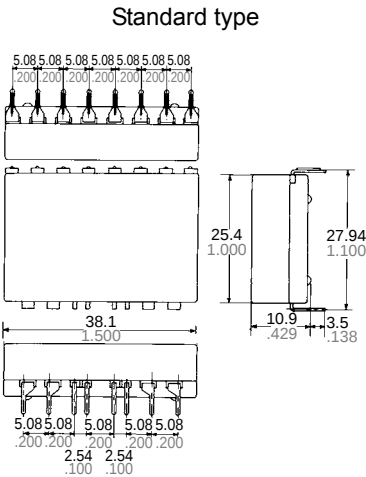
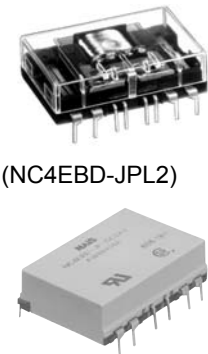
Schematic (Top view)



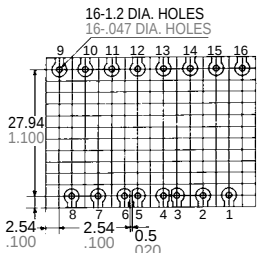
Deenergized position

General tolerance: $\pm 0.5 \pm .020$

4C 2 coil latching
(NC4D-JPL2)



PC board pattern (Copper-side view)



Tolerance: $\pm 0.1 \pm .004$

Schematic (Top view)

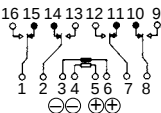
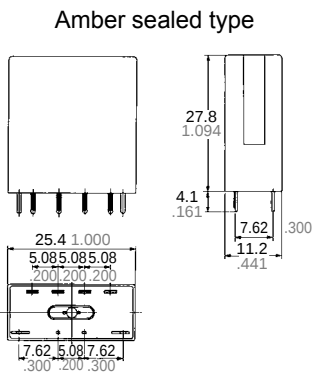
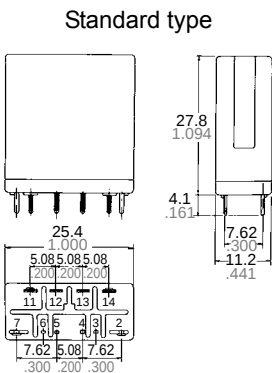


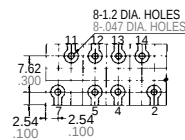
Diagram shows the "reset" position when terminals 3 and 6 are energized. Energize terminals 4 and 5 to transfer contacts.

General tolerance: $\pm 0.5 \pm .020$

Slim series
PC board series
2C single side stable
(NC2D-P)

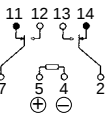


PC board pattern (Copper-side view)



Tolerance: $\pm 0.1 \pm .004$

Schematic (Bottom view)



Deenergized position

General tolerance: $\pm 0.5 \pm .020$