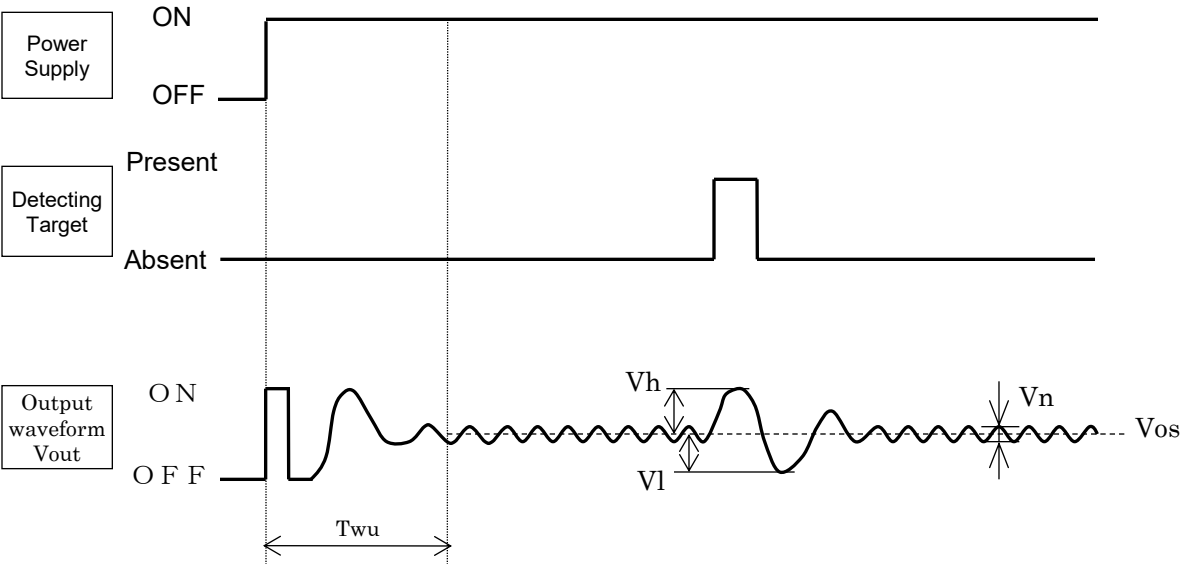


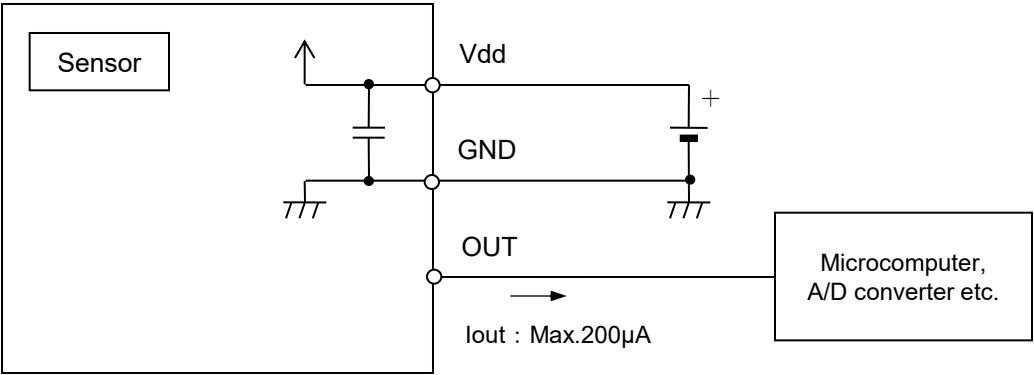
4-3 Timing Chart



【Durations】

T_{wu} : Circuit Stability Time = About 30s(max.)
 During the stage, the output's status is undefined (ON/OFF) and detection is therefore not guaranteed.

4-4 Wiring Diagram



Head the following precautions to prevent injury or accidents.

- (SKC0410-P01.02.140701)