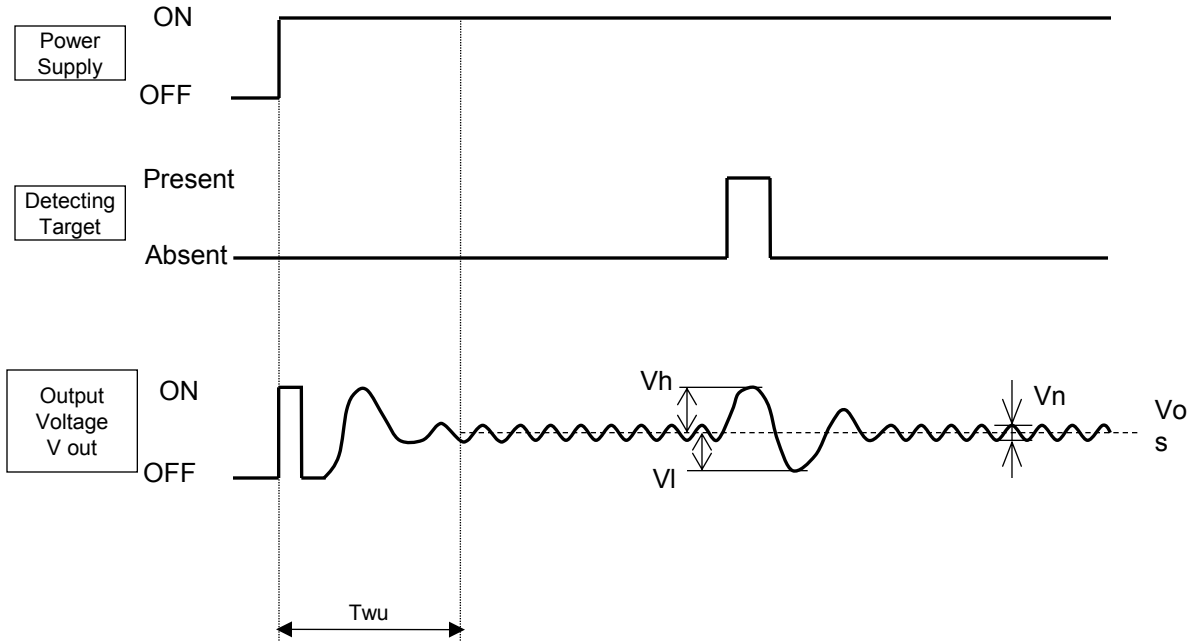


4-4 Timing Chart



【Durations】

T_{wu} : Circuit Stability Time: About 30s. (max.)

During this stage, the output's status is undefined (ON/OFF) and detection is therefore not guaranteed.

4-5 Detection Area

