

Control Integrated POver System (CIPOS™)

IKCM20L60GA

Datasheet

Table of contents

Table of contents	2
CIPOS™ Control Integrated POver System	3
Features	3
Target Applications.....	3
Description	3
System Configuration	3
Pin Configuration.....	4
Internal Electrical Schematic.....	4
Pin Assignment	5
Pin Description	5
HIN(U, V, W) and LIN(U, V, W) (Low side and high side control pins, Pin 7 - 12).....	5
VFO (Fault-output and NTC, Pin 14)	6
ITRIP (Over current detection function, Pin 15).....	6
VDD, VSS (Low side control supply and reference, Pin 13, 16)	6
VB(U, V, W) and VS(U, V, W) (High side supplies, Pin 1 - 6).....	6
NW, NV, NU (Low side emitter, Pin 17 - 19)	6
W, V, U (High side emitter and low side collector, Pin 20 - 22)	6
P (Positive bus input voltage, Pin 23).....	6
Absolute Maximum Ratings	7
Module Section	7
Inverter Section.....	7
Control Section	7
Recommended Operation Conditions	8
Static Parameters	9
Dynamic Parameters	10
Bootstrap Parameters	10
Thermistor	11
Mechanical Characteristics and Ratings	11
Circuit of a Typical Application	12
Switching Times Definition	13
Electrical characteristic	14
Package Outline.....	15
Revision history	16