# 1. CP430, CP470, CP474, CP770 and CP774

# 1.1 Order data

|               | and a second sec |                                | al a   |
|---------------|--|--------------------------------|--|
|               | -14  | 24                             |  |
|               |  |                                |  |
|               | CP430, CP470, CP770  | CP474, CP7                     | 774  |
| Model number  | Short description  |                                | 1 <sub>0</sub> ,   |
| 7CP430.60-1   | 2003 CPU, 100 KB SRAM, 256 KB FlashP<br>CAN: electrically isolated, network capable  |                                |  |
| 7CP470.60-2   | 2003 CPU, 350 KB SRAM, 512 KB FlashP<br>CAN: electrically isolated, network capable  |                                |  |
| 7CP474.60-2   | 2003 CPU, 750 KB SRAM, 512 KB FlashP<br>CAN: electrically isolated, network capable  |                                |  |
| 7CP770.60-1   | 2003 CPU, 100 KB SRAM, 256 KB FlashP<br>CAN: electrically isolated, network capable  |                                |  |
| 7CP774.60-1   | 2003 CPU, 100 KB SRAM, 512 KB FlashP<br>CAN: electrically isolated, network capable  |                                |  |
|               | Optional accessories   | See. See                       | . So   |
| 4A0006.00-000 | Lithium battery, 3 V / 950 mAh, button cell<br>Note: Backup battery included in delivery   | And Maria                      | ACTION IN CONTRACT   |
| 0AC201.9      | Lithium batteries, 5 pcs., 3 V / 950 mAh, b  | utton cell                     |  |
| 0G0001.00-090 | Cable PC <-> PLC/PW, RS232, online cab   | le                             | 2  |
| 7AC911.9      | Bus connector, CAN   | 9                              | S.   |
| 0AC912.9      | Bus adapter, CAN, 1 CAN interface  |                                | 5°   |
| 0AC913.92     | Bus adapter, CAN, 2 CAN interfaces, inclu  | ding 30 cm attachment cable (D | OSUB connector)  |
| 0MC111.9-1    | PC card, 2 MB FlashPROM  | S                              | . S.   |
| 0MC112.9-1    | PC card, 4 MB FlashPROM  | 245                            | State of the second sec |
|               |  |                                |  |

Table 1: Order data

# 1.2 Technical data

| 1.2 Technical data  |  |   |  |
|---|--|---|--|
| Name  | CP430  | CP470 / CP770   | CP474 / CP774  |
| Short description   | 0P430  | CF4/0/CF//0   | CF4/4/ CF//4   |
|   | and and a second | OPU   | and the second sec |
| System module<br>Interfaces   | 21   | CPU   | 20   |
|   |  | 1x RS232, 1x CAN bus  |  |
| Processor   | a North Contraction of the second sec |   | X  |
| Fastest task class cycle time   |  | 1 ms  |  |
| Typical instruction cycle time  | 1.6 µs   | 1.6 µs  | 0.8 µs   |
| Standard memory<br>User RAM<br>System PROM<br>User PROM   | 100 KB SRAM<br>256 KB FlashPROM<br>256 KB FlashPROM  | 350/100 KB SRAM<br>512/256 KB FlashPROM<br>512/256 KB FlashPROM     | 750/100 KB SRAM<br>512 KB FlashPROM<br>512 KB FlashPROM  |
| Data buffering<br>Backup battery<br>Buffer current<br>Typical                                     | Lithium battery 3 V / 950 mAh  | Lithium battery 3 V / 950 mAh<br>1.6 µA                             | Lithium battery 3 V / 950 mAl<br>2.2 µA  |
| Maximum   | 60 µA  | 60 µA   | 110 µA   |
| Hardware watchdog   | Sta  | Yes   | 5  |
| Voltage monitoring  | Internal sup   | ply monitored for overvoltage and u                                 | undervoltage   |
| Peripherals   | So and a second s  | S.  | . S  |
| Real-time clock<br>Resolution   | A. A   | Nonvolatile memory<br>1 s   | 444  |
| Status indicators   |  | LEDs  |  |
| I/O bus interface   | 2  | 9-pin DSUB socket   | , Q  |
| Slots for screw-in modules<br>Suitable for IF modules   | No   | No  | 4<br>1 - 3   |
| Interfaces  | . 1 <sup>0</sup>   |   | J.C.   |
| Interface IF1<br>Type<br>Electrical isolation<br>Design<br>Max. distance<br>Max. transfer rate    | South Street   | RS232<br>No<br>9-pin DSUB plug<br>15 m / 19200 bit/s<br>57.6 kBit/s | ANNO DE C  |
| Interface IF2<br>Type<br>Electrical isolation<br>Design<br>Max. distance<br>Maximum transfer rate | -oautomatike.P   | CAN<br>Yes<br>9-pin DSUB plug<br>1000 m<br>500 kBit/s               | SP.  |
| General information   | S. A.  | and in  | en la companya da companya   |
| Operation on module slot  | 35 1   | 3 <sup>25</sup> 1   | 1 + 2  |
| Logical module slots  | Max. 4   | Max. 8  | Max. 12  |
| analog module slots   | Max. 2   | Max. 4  | Max. 4   |
| Possible module addresses for analog  | 1-4  | 1-8   | 1 - 8  |

Table 2: Technical data

| Name  | CP430                      | CP470 / CP770                       | CP474 / CP774                      |
|---|----------------------------|-------------------------------------|------------------------------------|
| Visual Components capability  | J. S.                      | No                                  | 10                                 |
| ACOPOS capability   | No                         | No                                  | Yes                                |
| Certification   | July 1                     | CE, C-UL-US, GOST-R                 | AN CONTRACT                        |
| Mechanical characteristics  | 2                          | 20                                  | 20                                 |
| Dimensions  | System 20                  | 03 single-width                     | System 2003 double-width           |
| Protection type   | 10°                        | IP20                                | 2                                  |
| Operating temperature<br>Horizontal installation<br>Vertical installation | tonaty                     | 0°C to +60°C<br>0°C to +50°C        | , HOT                              |
| Storage temperature   | 20                         | -25°C to +60°C                      | . So.                              |
| Relative humidity   | AN                         | 5 to 95%, non-condensing            | and it                             |
| Comment   | 14                         | Backup battery included in delivery | 1 22                               |
| Power supply  | CP430                      | CP470/CP474                         | CP770/CP774                        |
| Input voltage<br>Minimum<br>Rated<br>Maximum                              | 18 VDC<br>24 VDC<br>30 VDC | 18 VDC<br>24 VDC<br>30 VDC          | 85 VAC<br>100 - 240 VAC<br>264 VAC |
| Input voltage frequency   |                            | - 55                                | 47 - 63 Hz                         |
| Power consumption   | Max. 9.5 W                 | Max. 20 W                           | Max. 20 W                          |
| Output power for I/O ports  | 7 W <sup>1)</sup>          | 14/12.6 W <sup>1)</sup>             | 14/12.6 W <sup>1)</sup>            |

Table 2: Technical data (cont.)

1) Integrated power supply on pin 4 of the RS232 interface for simple PANELWARE controllers, e.g. P126.

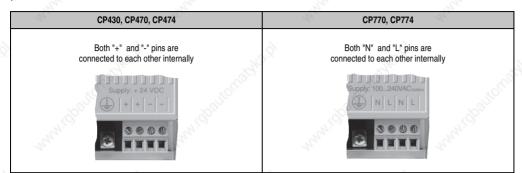
# 1.3 Status indicators

| LED                   | Meaning  |                       |
|-----------------------|--|-----------------------|
| CAN                   | Data transfer to or from CAN controller  | 345                   |
| RS232                 | Indicates if data is being transmitted or received   | 44                    |
| ERR                   | Lit when in Service mode   |                       |
| RUN                   | Lit in RUN and in Service mode   |                       |
| RDY                   | Lit when in Service mode   |                       |
| MODE                  | Lit when programming FlashPROM   | 5                     |
| 1, 2, 3, 4<br>Not lit | These LEDs show the operating state of the respective screw-in module.<br>Screw-in module defective or not inserted                                  | . Apaule              |
| Blinking slowly       | Communication error with screw-in module<br>Exception: On the IF361 and IF371 modules, a slow-blinking LED means that frame dr<br>running correctly. | iver communication is |
| Blinking quickly      | Screw-in module is new or has been exchanged with another module type  |                       |
| Lit                   | Screw-in module is ready for operation   |                       |

Table 3: Status indicators

## 1.4 Power supply

The CPUs are either supplied with 24 VDC or with 100 to 240 VAC. The pin assignments are printed on the module.





## 1.5 Interfaces

The CPU has two interfaces:



Figure 1: Interfaces

# 1.6 CAN bus

The electrically isolated standard fieldbus interface is used for the following tasks:

- · Communication with other control systems
- System decentralization and remote I/O expansion using System 2003 components and a CAN bus controller

We recommend using the 7AC911.9 T-connector for coupling to a CAN network. A terminal resistor is integrated into the T-connector for the bus termination, which can be switched on or off.

|   | de la           | . S <sup>or</sup> | CAN interface | Ser .           |         |
|---|-----------------|-------------------|---------------|-----------------|---------|
| - | Pin             | Assignment        |               | 0               | aller.  |
|   | <sup>10</sup> 1 | NC                | 24            |                 | 24      |
|   | 2               | CAN_L             |               | 9-pin DSUB plug |         |
| 5 | 3               | CAN_GND           | 13.8          | 9-pin DOOD plug |         |
|   | 4               | NC                | 201           | 6 1             |         |
|   | 5 5             | NC                | ×-            |                 | . S     |
|   | 6               | Reserved          |               | 9 5             | 108/101 |
|   | 7               | CAN_H             | 3             | 19.<br>19.      |         |
|   | 8               | NC                | 44            |                 | AS AN   |
|   | 9               | NC                | ]             |                 |         |

Table 5: Pin assignments for CAN interface

### 1.7 RS232 interface

This interface, which is not electrically isolated, is primarily intended for programming the CPU. The RS232 interface can also be used as a general interface (e.g. P126 visualization, printer, bar code reader, etc.).

|     |                      |                 | RS232 interfac | e               |       |
|-----|----------------------|-----------------|----------------|-----------------|-------|
| Pin | Assignment           | Name            | 10.2           | 18.2            |       |
| 1   | NC                   | Reserved        | 100            |                 |       |
| 2   | RXD                  | Receive signal  | 5              | 9-pin DSUB plug | . 6   |
| 3   | TXD                  | Transmit signal |                | 9-pin DOOB plug | 10311 |
| 4   | +5 VDC / max. 500 mA | Panel supply    |                | 6 1             |       |
| 5   | GND                  | Ground          |                |                 | 3527  |
| 6   | NC                   | Reserved        |                | 9 💽 5           |       |
| 7   | RTS                  | Request To Send | 6              |                 |       |
| 8   | CTS                  | Clear To Send   | No.            |                 | 2     |
| 9   | GND                  | Ground          | Ser'           |                 | S.    |



## 1.8 Mode switch



Figure 2: Mode switch

The operating mode is set with the Mode switch. The switch setting can be evaluated by the application program at any time. The operating system only interprets the switch position when switched on.

| Switch position | Operating mode | Description   |
|-----------------|----------------|---|
| \$0             | Boot           | In this switch position, the operating system can be installed via the RS232 interface configured as<br>online interface. User Flash is only deleted after the update begins.     |
| \$1 - \$8       | Run            | RUN mode, the application is running. These switch settings are freely available for use in an application (e.g. CAN node number).  |
| \$9 - \$E       | Reserved       | Reserved for B&R expansions – these settings are not allowed to be used!  |
| \$F             | Diagnostics    | The CPU boots in diagnostics mode. Program sections in User RAM and User FlashPROM are not initialized. After diagnostics mode, the CPU always boots with a <b>cold restart</b> . |

Table 7: Operating modes

## 1.9 Programming the System Flash

### 1.9.1 General information

CPUs are delivered with a runtime system. The node number switch is set to switch position \$0 when delivered i.e. bootstrap loader mode is set.

A corresponding switch position must be set (\$1 to \$8) in order to boot the PLC in RUN mode. A runtime system update is only possible in RUN mode.

### 1.9.2 Runtime system update

The runtime system can be updated using the programming system. When updating the runtime system (online runtime system update), the following procedure must be carried out:

- 1) An online runtime system update is only possible if the processor is in RUN mode. To do this, the set node number must be in the range from \$1 to \$8.
- 2) Switch on the power.
- 3) The runtime system update is performed via the existing online connection. An online runtime update is possible using the serial RS232 onboard interface.
- 4) Start B&R Automation Studio.

- Start the update procedure by calling the Services command from the Project menu. Select Transfer Operating System... from the menu shown. Now follow the instructions from B&R Automation Studio.
- 6) A dialog box is displayed for configuring the runtime system version. The runtime system version is already pre-selected by the user's project settings. Using the drop-down menu, the runtime system versions stored in the project can be selected. Clicking on the **Browse** button allows the selected runtime system version to be loaded from the hard drive or from the CD.

Pressing **Next** > opens a pop-up window, which allows the user to select whether the modules should also be downloaded with SYSTEM ROM as the target memory during the following runtime system update. Otherwise, modules can also be downloaded later together with an application download.

After pressing **Next** >, a dialog box appears where the user can set the CAN transfer rate, CAN ID and the CAN node number (the CAN node number set here is only relevant if an interface module does not have a CAN node number switch). The CAN node number must be between decimal 01 and 99. It's made up of the switch position 1 - 8 and a decimal offset entry. Assigning a unique node number is especially important with online communication over a CAN network (INA2000 protocol).

7) The update procedure is started by pressing **Next** >. The update progress is shown in a message box.

# Information:

User flash is cleared.

- When the update procedure is complete, the online connection is automatically reestablished.
- 9) The PLC is now ready for use.

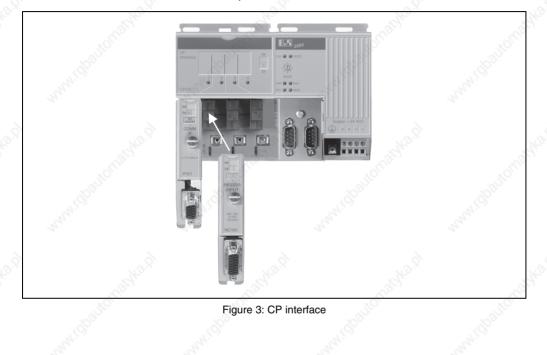
A runtime system update is not only possible using an online connection, but also using a CAN network or a serial network (INA2000 protocol), depending on the system configuration.

## 1.10 CP interface

The CPUs CP474 and CP774 are equipped with four slots for screw-in modules. The required screw-in modules are inserted into the CP interface and screwed firmly into place.

TPU mode is possible on all 4 slots.

The screw-in interface modules can be operated in slots 1, 2 and 3.



## 1.10.1 Overview

| Module      | Туре           | Description                   | and the second se | AN CONTRACT  |
|-------------|----------------|-------------------------------|---|--|
| 7AI261.7    | Analog IN      | 1 input used to evaluate a    | full-bridge strain gauge  | 24.  |
| 7AI294.7    | Analog IN      | 4 inputs for potentiometer of | displacement gauge  |  |
| 7AI351.70   | Analog IN      | 1x ±10 V or 1x 0 - 20 mA (    | (1x ±20 mA also possible) potentiometer operation   | n  |
| 7AI354.70   | Analog IN      | 4x ±10 V                      | 20  |  |
| 7AI774.70   | Analog IN      | 4x 0 - 20 mA (4x ±20 mA a     | also possible)  | 30,  |
| 7AO352.70   | Analog OUT     | 2x ±10 V / 0 - 20 mA          | 19 <sup>10</sup>  | 500  |
| 7AT324.70   | Analog IN      | 4x temperature sensor (PT     | T100, PT1000, KTY10 or KTY84)   | A.C.   |
| 7AT352.70   | Analog IN      | 2x PT100 3-line               | all and a second se  | AND I WELL   |
| 7AT664.70   | Analog IN      | 4x thermocouple               |   |  |
| 7DI135.70   | Digital IN     | 4x 24 VDC, 50 kHz             | 6 6   |  |
| 7DI138.70   | Digital IN     | 10x 24 VDC, 20 kHz            | ALC: NO   |  |
| 7DI140.70   | Digital IN     | 10x 24 VDC, 50 kHz            | -Clor   | 3.   |
| 7DO135.70   | Digital OUT    | 4x 12 - 24 VDC, 0.1 A, 100    | ) kHz   | 250  |
| 7DO138.70   | Digital OUT    | 8x 24 VDC, 0.5 A              | Š.  | S°   |
| 7DO139.70   | Digital OUT    | 8x 12 - 24 VDC, 0.5 A         |   | and the second s |
| 7DO164.70   | Digital OUT    | 4x 48 - 125 VAC, 50 mA, z     | zero voltage input  | 20   |
| 7IF311.7    | Interface      | 1x RS232                      | X X   |  |
| 7IF321.7    | Interface      | 1x RS485/RS422                | 2.2 LO.2  |  |
| 7IF361.70-1 | Interface      | 1x Profibus DP slave          | 200   | 2  |
| 7IF371.70-1 | Interface      | 1x CAN                        | 205   | 205  |
| 7NC161.7    | Encoder module | 1x 100 kHz, 5 / 24 VDC        | 30  | 100  |

The following screw-in modules can be used on the CP interface.

Table 8: Screw-in modules

#### 1.10.2 Commands

The following commands can be used on the CP Interface:

- Reading the screw-in module type
- Switching off automatic mode
- Switching on automatic mode

## 1.11 Legend strips

A legend sheet can be slid into the front of the CPUs CP474 and CP774 from above. These sheets can be used for labeling the screw in modules.

### 1.12 Data / real-time buffering

The battery voltage is checked cyclically. The cyclic load test of the battery does not considerably shorten the battery life, instead it gives an early warning of weakened buffer capacity.

The status information, "Battery OK" is available from the B&R TRAP function "SYS\_battery".

## 1.13 System variable SYS2003

#### 1.13.1 General information

The system variable SYS2003 is a structure containing the elements "io\_scan" and "io\_refresh". It must be declared in a task as PLC global.

| Element    | Variable type | Description  | 23                                |
|------------|---------------|--|-----------------------------------|
| io_scan    | UINT          | Duration of the last I/O cycle in µs                           | × .                               |
| io_refresh | SINT          | 0 I/O data is more than one cycle old<br>1 I/O data is current | . 3 <sup>1</sup> 2 <sup>1</sup> 2 |

Table 9: System variable SYS2003

# Information:

If digital IO data points are used in the HSTC (high speed task class), the system variable SYS2003 will also be placed in the HSTC. Values in lower task classes will therefore not be consistent.

If no digital I/O data points are placed in the HSTC, the SYS2003 variable will use the 10 ms operating system clock.

## 1.14 Changing the battery

### 1.14.1 Battery data

| 4 N N                                       | 1 pcs.<br>5 pcs. | ANN.   |
|---|------------------|--|
| Lithium battery, 3 V / 950 mAh, button cell |                  |  |
| -20 to +60°C                                |                  |  |
| Max. 3 years at 30°C                        |                  |  |
| 0 to 95% (non-condensing)                   |                  |  |
|   | niti Maria       | 5 pcs.<br>Lithium battery, 3 V / 950 mAh, button<br>-20 to +60°C<br>Max. 3 years at 30°C |

Table 10: Battery data

#### 1.14.2 Buffer duration

| Buffer current | CP470 / CP770 | CP474 / CP774 |      |
|----------------|---------------|---------------|------|
| Typical        | 1.6 µA        | 2.2 μΑ        | 0    |
| Maximum        | 60 µA         | 110 µA        | Ser. |

Table 11: Buffer duration

# Information:

#### B&R recommends changing the batteries after five years of operation.

#### 1.14.3 Procedure

The product design allows the battery to be changed with the PLC switched either on or off. In some countries, safety regulations do not allow batteries to be changed while the module is switched on.

# Information:

Data stored in RAM will be lost if the battery is changed with the PLC switched off.

- 1) Touch the mounting rail or ground connection (not the power supply!) in order to discharge any electrostatic charge from your body.
- 2) Remove the cover from the lithium battery holder using a screwdriver.

Remove the battery from the holder by pulling the removal strip (do not use uninsulated tools
-> risk of short circuiting). The battery should not be held by its edges. <u>Insulated</u> tweezers
may also be used for removing the battery.

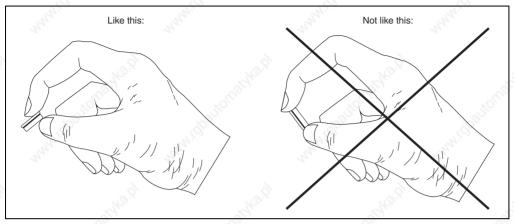


Figure 4: Handling the battery

4) Insert the new battery with correct polarity. The removal strip should be protruding from the battery holder and the "+" side of the battery should be facing downward. In order to be able to remove the battery again in future, the removal strip must protrude from the upper side of the battery.

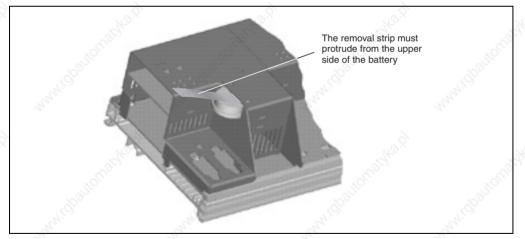


Figure 5: Removal strips

5) Now wrap the end of the removal strip over the top of the battery and insert it underneath the battery so that it does not protrude from the battery holder.

6) Replace cover. Ensure that the slot in the edge of the cover faces the front of the module (1). Insert the upper edge of the cover in the battery holder opening (2). Press the lower end of the cover home firmly (3).



Figure 6: Replace cover

# Information:

Lithium batteries are considered hazardous waste. Used batteries should be disposed of accordingly.