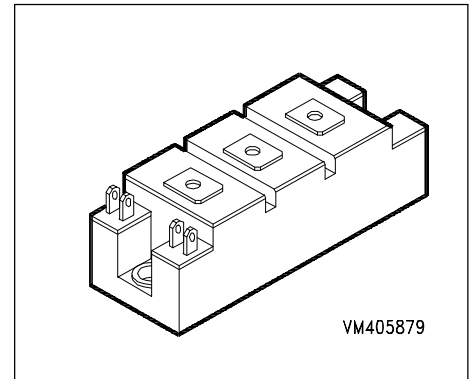


IGBT Power Module

- Single switch with chopper diode at collector
- Including fast free-wheeling diodes
- Package with insulated metal base plate



Type	V_{CE}	I_C	Package	Ordering Code
BSM 50 GAL 120 DN2	1200V	78A	HALF BRIDGE GAL 1	C67076-A2010-A70

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE}	1200	V
Collector-gate voltage	V_{CGR}	1200	
$R_{GE} = 20 \text{ k}\Omega$			
Gate-emitter voltage	V_{GE}	± 20	
DC collector current	I_C	78	A
$T_C = 25 \text{ }^\circ\text{C}$			
$T_C = 80 \text{ }^\circ\text{C}$		50	
Pulsed collector current, $t_p = 1 \text{ ms}$	I_{Cpuls}	156	
$T_C = 25 \text{ }^\circ\text{C}$			
$T_C = 80 \text{ }^\circ\text{C}$		100	
Power dissipation per IGBT	P_{tot}	400	W
$T_C = 25 \text{ }^\circ\text{C}$			
Chip temperature	T_j	+ 150	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 ... + 125	
Thermal resistance, chip case	R_{thJC}	≤ 0.3	K/W
Diode thermal resistance, chip case	R_{thJCD}	≤ 0.6	
Diode thermal resistance, chip-case, chopper	R_{thJCDC}	≤ 0.5	
Insulation test voltage, $t = 1 \text{ min.}$	V_{is}	2500	Vac
Creepage distance	-	20	mm
Clearance	-	11	
DIN humidity category, DIN 40 040	-	F	sec
IEC climatic category, DIN IEC 68-1	-	40 / 125 / 56	

Electrical Characteristics, at $T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Gate threshold voltage $V_{GE} = V_{CE}, I_C = 2\text{ mA}$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter saturation voltage $V_{GE} = 15\text{ V}, I_C = 50\text{ A}, T_j = 25\text{ °C}$ $V_{GE} = 15\text{ V}, I_C = 50\text{ A}, T_j = 125\text{ °C}$	$V_{CE(sat)}$	-	2.5 3.1	3 3.7	
Zero gate voltage collector current $V_{CE} = 1200\text{ V}, V_{GE} = 0\text{ V}, T_j = 25\text{ °C}$ $V_{CE} = 1200\text{ V}, V_{GE} = 0\text{ V}, T_j = 125\text{ °C}$	I_{CES}	-	0.8 4	1 -	mA
Gate-emitter leakage current $V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	I_{GES}	-	-	400	nA

AC Characteristics

Transconductance $V_{CE} = 20\text{ V}, I_C = 50\text{ A}$	g_{fs}	23	-	-	S
Input capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{iss}	-	3.3	-	nF
Output capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{oss}	-	0.5	-	
Reverse transfer capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{rss}	-	0.25	-	