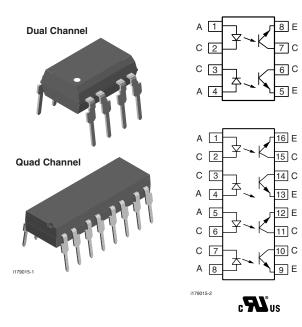


Vishay Semiconductors

Optocoupler, Phototransistor Output (Multichannel)



FEATURES

- CNY74-2H, CNY74-4H TTL compatible
- Transfer ratio, 35 % typical
- Coupling capacitance, 0.5 pF
- · Dual and quad channel
- · Industry standard DIP packages
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC





RoHS

AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- UL1577, file no. E52744, equivalent to CSA bulletin 5A

DESCRIPTION

The CNY74-2H, CNY74-4H is an optically coupled pair with a GaAlAs infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

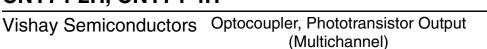
The CNY74-2H, CNY74-4H is especially for driving medium-speed logic, where it may be used to eliminate troublesome ground loop and noise problems. Also it can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CTR modulation.

The CNY74-2H has two isolated channels in a single DIP package; the CNY74-4H has four isolated channels per package.

ORDER INFORMATION					
PART	REMARKS				
CNY74-2H	CTR 50 % to 600 %, dual channel DIP-8				
CNY74-4H	CTR 50 % to 600 %, quad channel DIP-16				

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
INPUT							
Peak reverse voltage			V_{R}	3	V		
Forward continuous current			I _F	60	mA		
Power dissipation			P _{diss}	100	mW		
Derate linearly from 55 %				1.33	mW/°C		

CNY74-2H, CNY74-4H





ABSOLUTE MAXIMUM RATINGS								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
OUTPUT								
Collector emitter breakdown voltage			BV_{CEO}	70	V			
Emitter collector breakdown voltage			BV _{ECO}	7	V			
Power dissipation			P _{diss}	150	mW			
Derate linearly from 25 °C				2	mW/°C			
COUPLER								
Isolation test voltage	t = 1 s		V_{ISO}	5300	V_{RMS}			
	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$		R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C		R _{IO}	≥ 10 ¹¹	Ω			
Takal maalaa wa dhadhadhaa		CNY74-2H	P _{tot}	400	mW			
Total package dissipation		CNY74-4H	P _{tot}	500	mW			
Devicts linearly from 05 °C		CNY74-2H		5.33	mW/°C			
Derate linearly from 25 °C		CNY74-4H		6.67	mW/°C			
Creepage distance				≥ 7	mm			
Clearance distance				≥ 7	mm			
Storage temperature			T _{stg}	- 55 to + 150	°C			
Operating temperature			T _{amb}	- 55 to + 100	°C			
Lead soldering time at 260 °C				10	S			

Note

 T_{amb} = 25 °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 20 mA	V_{F}		1.3	1.5	V	
Reverse current	V _R = 3 V	I _R		0.1	100	μΑ	
Capacitance	V _R = 0 V	Co		25		pF	
OUTPUT							
Collector emitter breakdown voltage	I _C = 1 mA	BV _{CEO}	70			V	
Collector emitter leakage current	$V_{CE} = 5 \text{ V}, I_F = 0 \text{ A}$	I _{CEO}			100	nA	
Capacitance collector emitter	V _{CE} = 0 V, f = 1 Hz	C _{CE}		10		pF	
COUPLER							
Saturation voltage, collector emitter	$I_C = 2 \text{ mA}, I_F = 16 \text{ mA}$	V _{CEsat}		0.3	0.5	V	
Resistance (input to output)		R _{IO}		100		GΩ	
Capacitance (input to output)		C _{IO}		0.5		pF	

Note

 T_{amb} = 25 °C, unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
DC current transfer ratio	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	50		600	%	
DC current transfer ratio	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	60			%	



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SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	t _d		3		μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	t _r		3		μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	t _f		4.7		μs
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	ts		0.3		μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	t _{on}		6		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega \text{ (see figure 1)}$	t _{off}		5		μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 10 \text{ mA}, R_L = 1 \text{ k}\Omega \text{ (see figure 2)}$	t _{on}		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 10 \text{ mA}, R_L = 1 \text{ k}\Omega \text{ (see figure 2)}$	t _{off}		18		μs

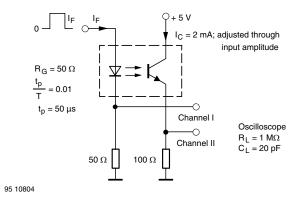


Fig. 1 - Test Circuit, Non-Saturated Operation

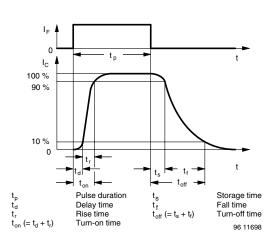


Fig. 3 - Switching Times

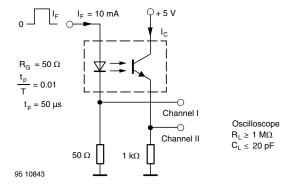


Fig. 2 - Test Circuit, Saturated Operation

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TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

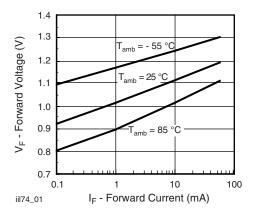


Fig. 4 - Forward Voltage vs. Forward Current

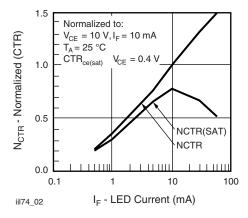


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

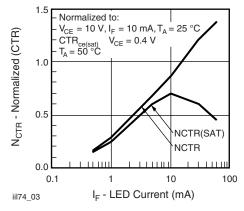


Fig. 6 - Normalized Non-Saturated and Saturated CTR vs. LED Current

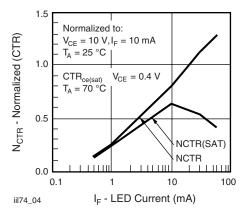


Fig. 7 - Normalized Non-Saturated and Saturated CTR vs. LED Current

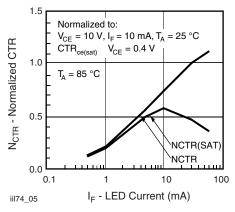


Fig. 8 - Normalized Non-Saturated and Saturated CTR vs. LED Current

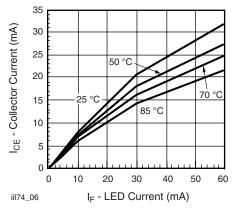


Fig. 9 - Collector Emitter Current vs. Temperature and LED Current



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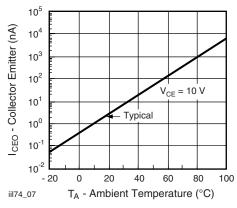


Fig. 10 - Collector Emitter Leakage Current vs.Temperature

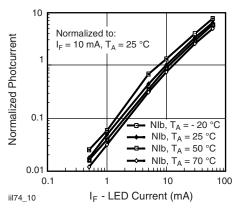


Fig. 13 - Normalized Photocurrent vs. I_F and Temperature

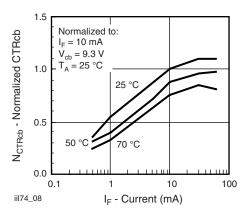


Fig. 11 - Normalized CTR_{cb} vs. LED Current and Temperature

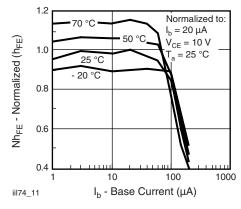


Fig. 14 - Normalized Non-Saturated h_{FE} vs. Base Current and Temperature

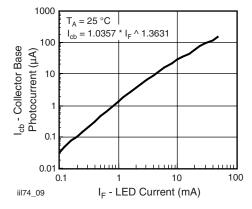


Fig. 12 - Collector Base Photocurrent vs. LED Current

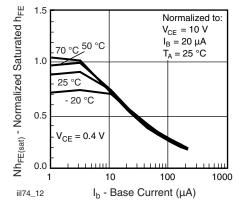


Fig. 15 - Normalized Saturated h_{FE} vs. Base Current and Temperature

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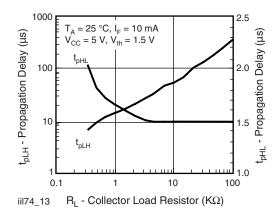
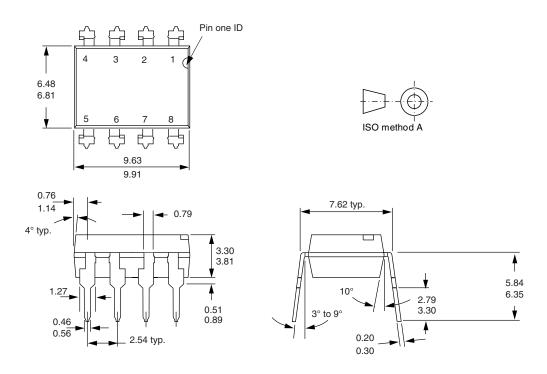


Fig. 16 - Propagation Delay vs. Collector Load Resistor

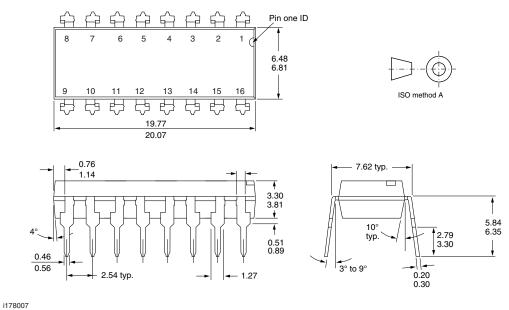
PACKAGE DIMENSIONS in millimeters



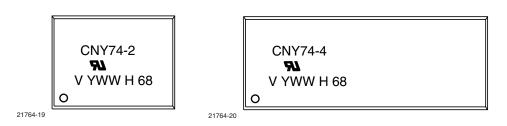


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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING



Note

CNY74-2H and CNY74-4H are marked as CNY74-2 and CNY74-4 respectively.



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Revision: 13-Jun-16 1 Document Number: 91000